

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
60V	2.6m Ω @10V	100A
	3.5m Ω @4.5V	

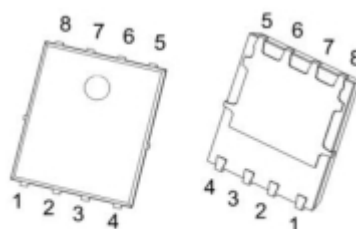
Feature

- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Fast switching and soft recovery

Applications

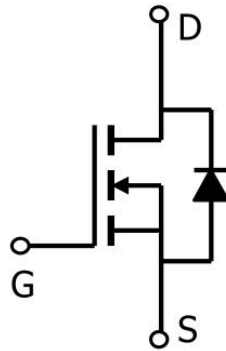
- Consumer electronic power supply
- Motor control
- Synchronous-rectification
- Isolated DC/DC convertor
- Invertors

Package



PDFN5x6-8L

Circuit diagram



Marking



60N03G : Product code
* :Month code.

Absolute maximum ratings

(T_a=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain source voltage	V _{DS}	60	V
Gate source voltage	V _{GS}	±20	V
Continuous drain current ¹⁾	I _D	100	A
Pulsed drain current ²⁾	I _{DM}	400	A
Power dissipation ³⁾	P _D	140	W
Single pulsed avalanche energy ⁵⁾	E _{AS}	80	mJ
Thermal resistance, junction-case	R _{θJC}	0.89	°C/ W
Thermal resistance, junction-ambient ⁴⁾	R _{θJA}	62	°C
Operation and storage temperature	T _J , T _{STG}	-55~ +150	°C

Electrical characteristics

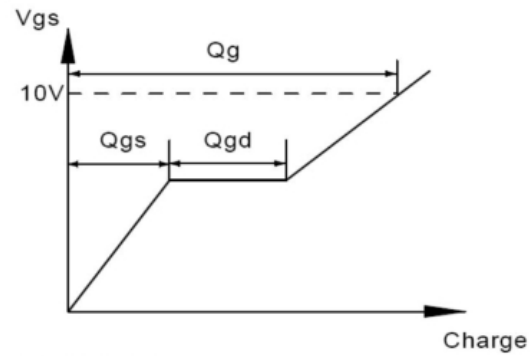
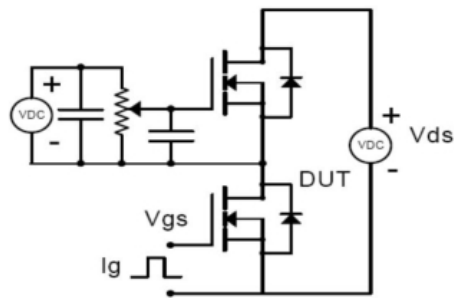
(T_A=25°C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV (BR)DSS	V _{GS} = 0V, I _D =250μA	60			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.3		2.5	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		2.6	3.2	mΩ
		V _{GS} =4.5V, I _D =10A		3.5	4.3	
Gate-source leakage current	I _{GSS}	V _{GS} = ±20V			±100	uA
Drain-source leakage current	I _{DSS}	V _{DS} =60V,V _{GS} = 0V			1	uA
Dynamic Characteristics						
Input capacitance	C _{iss}	V _{GS} =0V, V _{DS} =25V, f=100KHz		5377		pF
Output capacitance	C _{oss}			1666		
Reverse transfer capacitance	C _{rss}			77.7		
Turn-on delay time	T _{d(on)}	V _{GS} =10V, V _{DS} =30V, R _G =2Ω, I _D =25A		22.5		nS
Rise time	T _r			6.7		
Turn-off delay time	T _{d(off)}			80.3		
Fall time	T _f			26.8		
Switching Characteristics						
Total Gate Charge	Q _g	I _D =25A, V _{DS} =30V , V _{GS} =10V		66.1		pF
Gate-Source Charge	Q _{gs}			10.7		
Gate-Drain Charge	Q _{gd}			10.9		
Gate plateau voltage	V _{plateau}			2.9		V
Diode Characteristics						
Diode forward voltage	V _{SD}	V _{GS} =0V ,I _S =20A			1.3	V
Reverse recovery time	t _{rr}	I _S =25A,di/dt=100 A/μs		68.3		ns
Reverse recovery charge	Q _{rr}			73		nC
Peak reverse recovery current	I _{rrm}			1.9		A

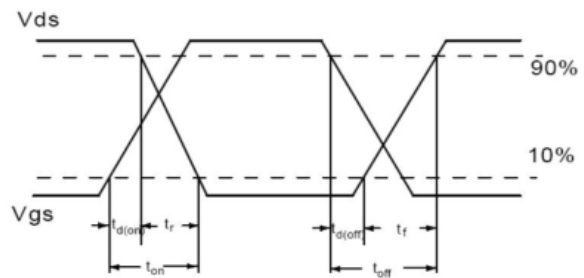
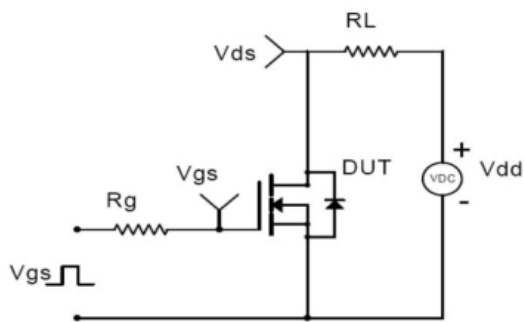
Note :

1. Calculated continuous current based on maximum allowable junction temperature.
2. Repetitive rating; pulse width limited by max. junction temperature.
3. Pd is based on max. junction temperature, using junction-case thermal resistance.
4. The value of R_{θJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
5. V_{DD}=50 V, R_G=25 Ω, L=0.3 mH, starting T_J=25 °C.

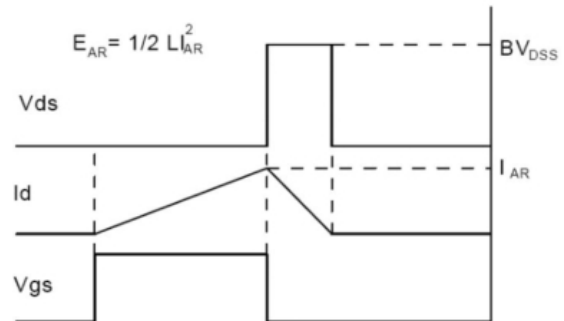
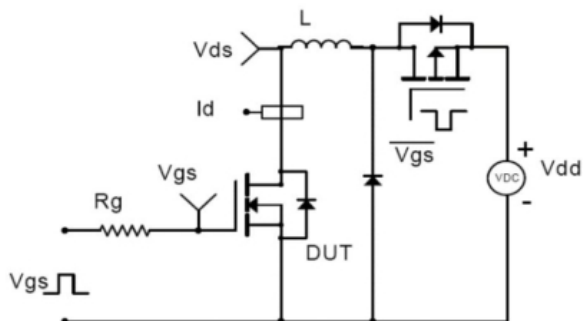
Typical Characteristics



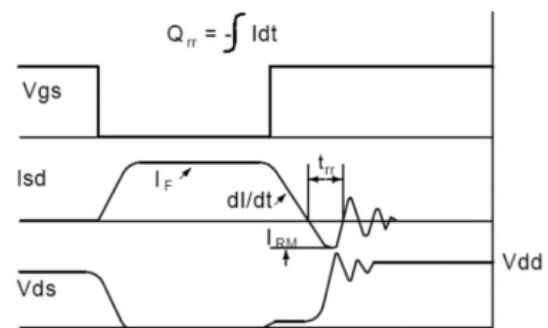
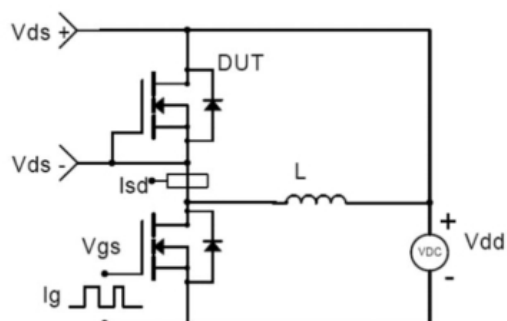
Gate charge test circuit & waveform



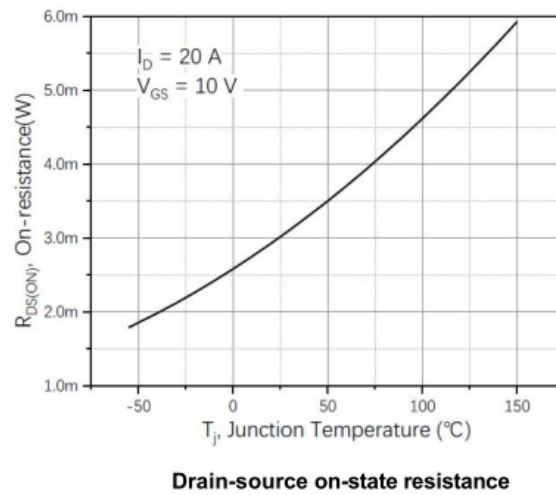
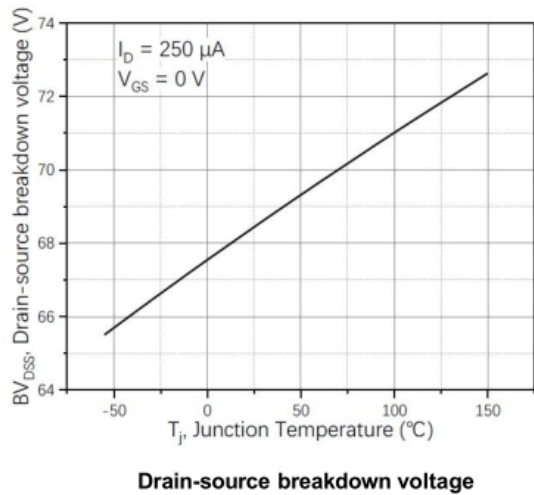
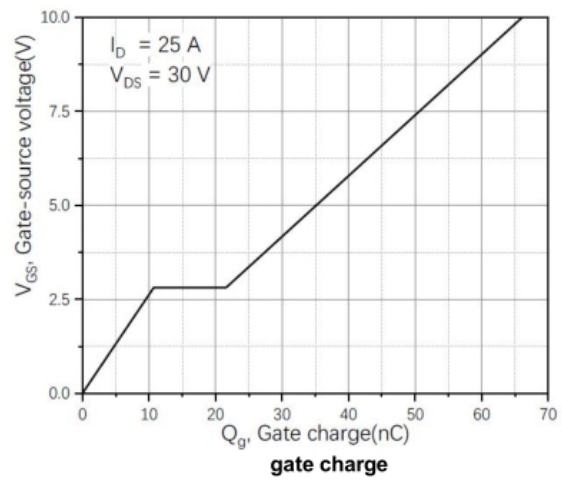
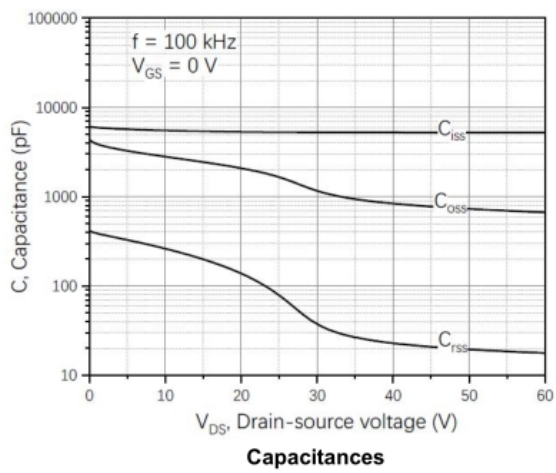
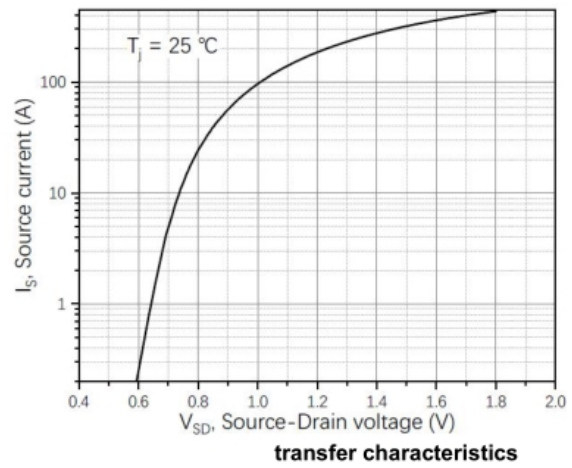
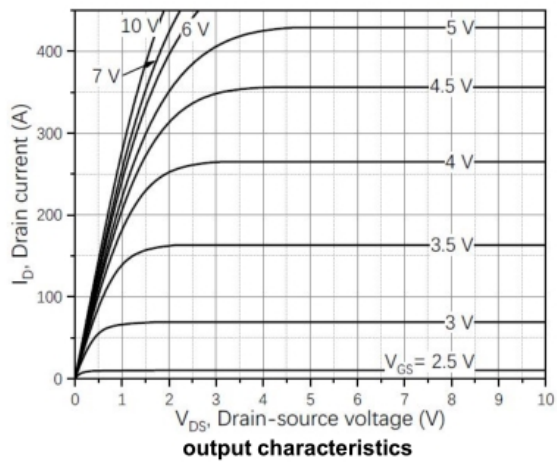
Switching time test circuit & waveforms

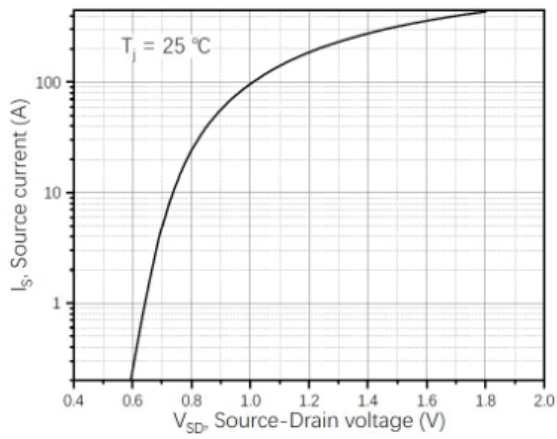


Unclamped inductive switching (UIS) test circuit & waveforms

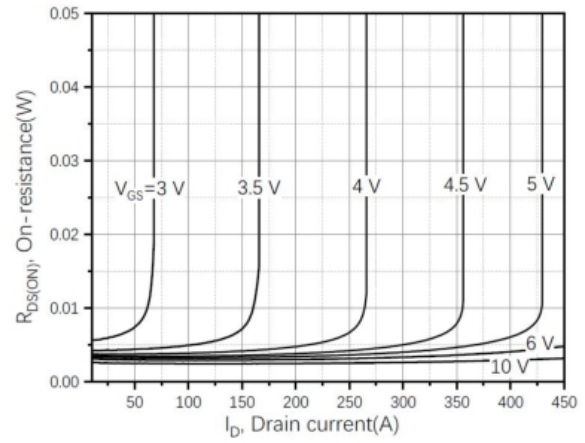


Diode reverse recovery test circuit & waveforms

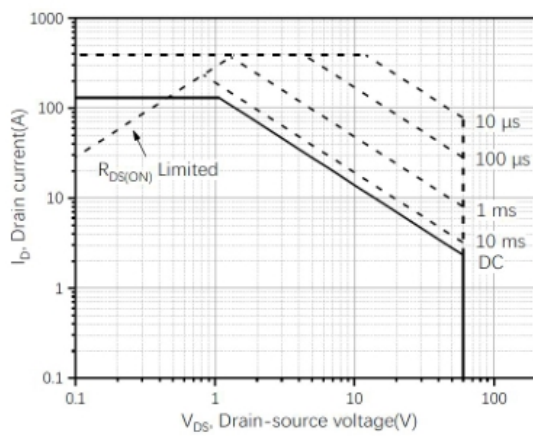




Forward characteristic of body diode

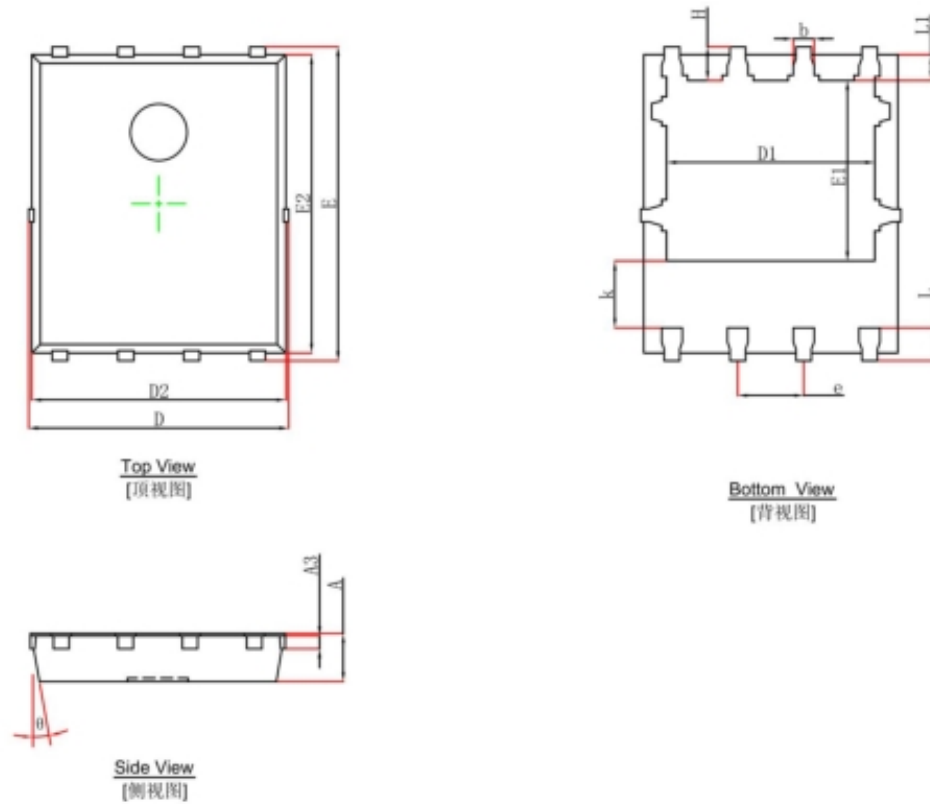


Drain-source on-state resistance



Safe operation area $T_c=25^\circ\text{C}$

PDFNWB5X6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°