

Product Summary

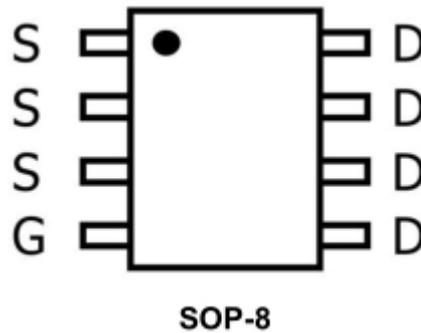
$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
-30V	11mΩ@-10V	-13A
	14mΩ@-4.5V	

Feature

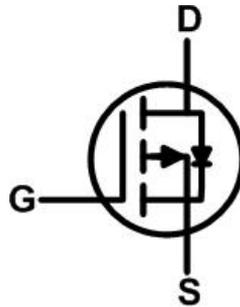
- TrenchFET Power MOSFET
- Excellent $R_{DS(on)}$ and Low Gate Charge

Applications

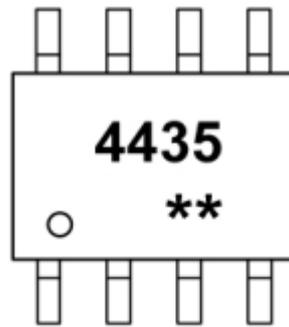
- Advanced trench process technology
- High density cell design for ultra-low on-resistance
- High power and current handing capability
- Ideal for Lion battery pack applications

Package


Circuit diagram



Marking



4435 =Device Code
 ** =Week Code

Absolute maximum ratings

($T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	-13	A
Pulsed Drain Current ¹⁾	I_{DM}	-50	A
Power Dissipation	P_D	2.5	W
Thermal Resistance from Junction to Ambient ²⁾	$R_{\theta JA}$	50	$^{\circ}\text{C}/\text{W}$
Junction Temperature	T_J	150	
Storage Temperature	T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Electrical characteristics

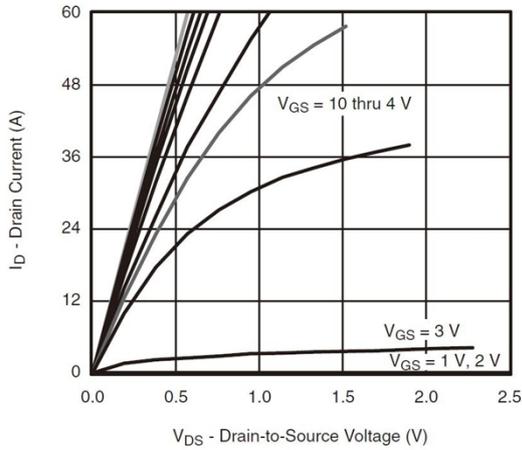
($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$BV_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-30			V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -30V, V_{GS} = 0V$			-1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	μA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.0	-1.6	-2.0	V
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -10A$		11	15	m Ω
		$V_{GS} = -5V, I_D = -7A$		14	25	
Dynamic Characteristics⁴⁾						
Input capacitance	C_{iss}	$V_{DS} = -15V, V_{GS} = 0V,$ $f = 1MHz$		2550		pF
Output capacitance	C_{oss}			450		
Reverse transfer capacitance	C_{rss}			390		
Switching Characteristics⁴⁾						
Turn-on Delay Time	$T_{d(on)}$	$V_{GEN} = -10V, V_{DD} = -15V,$ $R_{GEN} = 3.3\Omega, I_D = -1A$		33.8		nS
Turn-on Rise Time	T_r			35.8		
Turn-Off Delay Time	$T_{d(off)}$			72.8		
Turn-Off Fall Time	t_f			10.6		
Total gate charge	Q_g	$V_{DS} = -15V, V_{GS} = -4.5V,$ $I_D = -10A$		20		pF
Gate-source charge	Q_{gs}			5.1		
Gate-drain charge	Q_{gd}			7.3		
Source-Drain Diode Characteristics						
Body Diode Voltage	V_{DS}	$I_S = -1A, V_{GS} = 0V$			-1	V

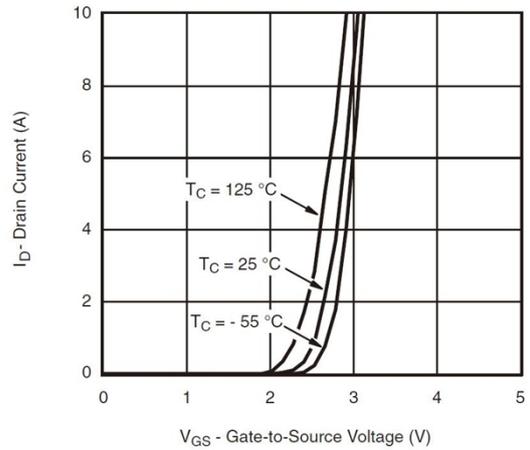
Notes:

1. Repetitive rating: Pulse width limited by junction temperature.
2. Surface mounted on FR4 board, $t \leq 10s$.

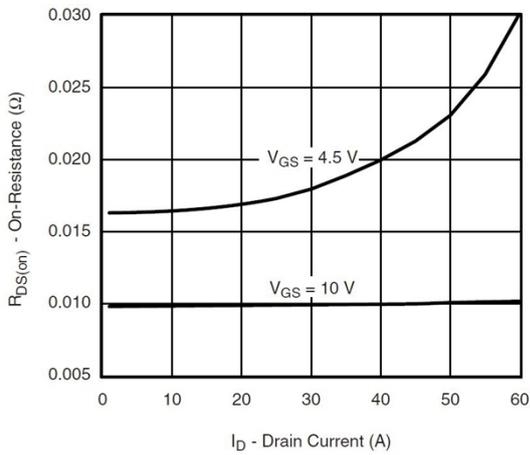
Typical Characteristics



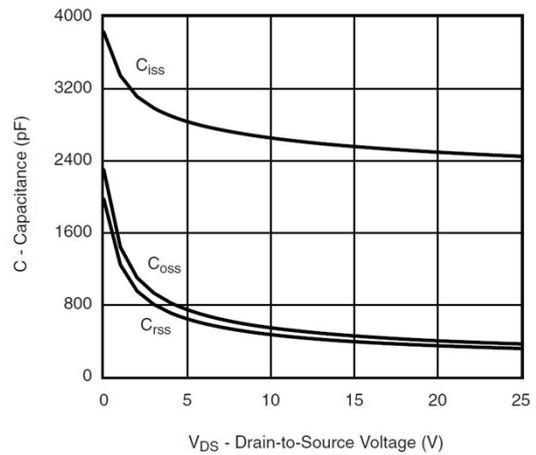
Output Characteristics



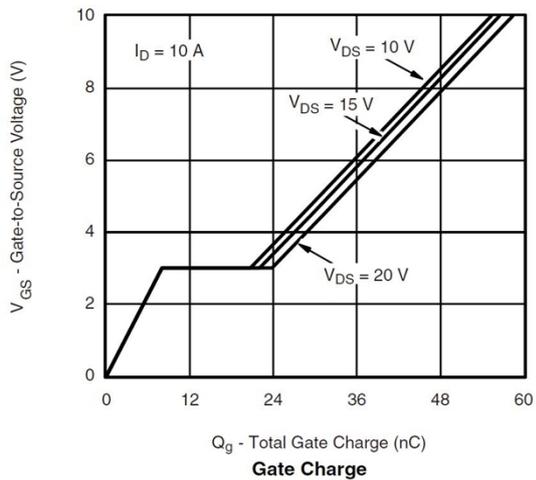
Transfer Characteristics



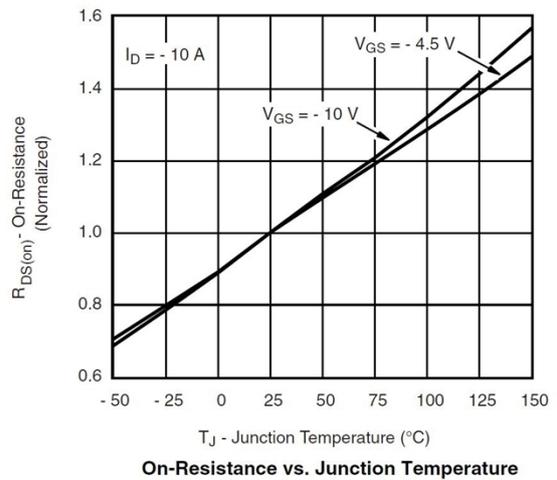
On-Resistance vs. Drain Current



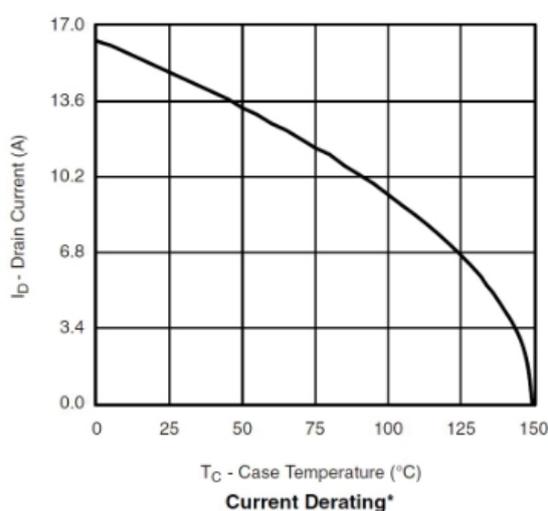
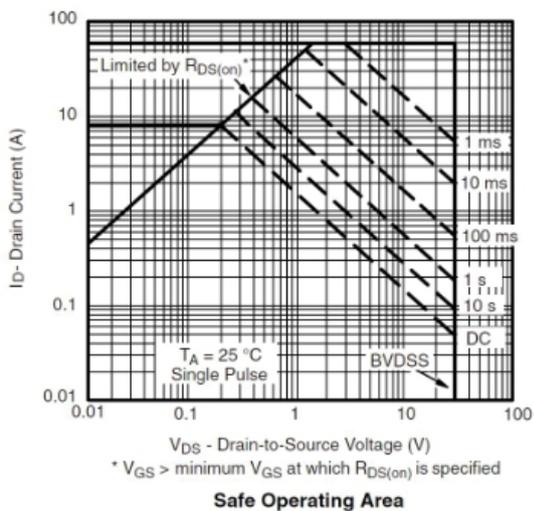
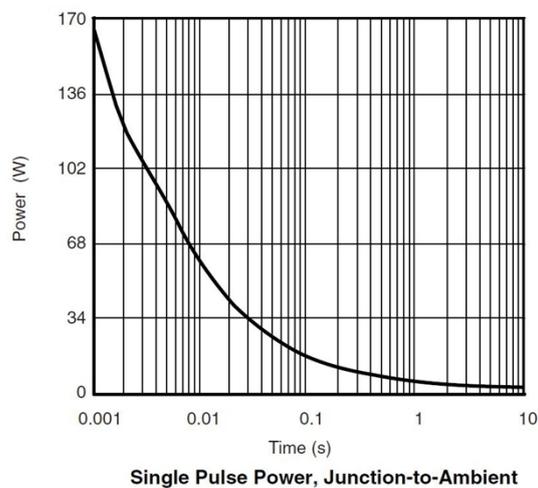
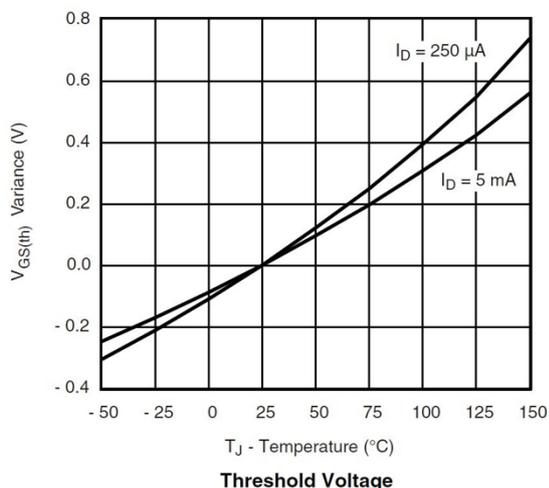
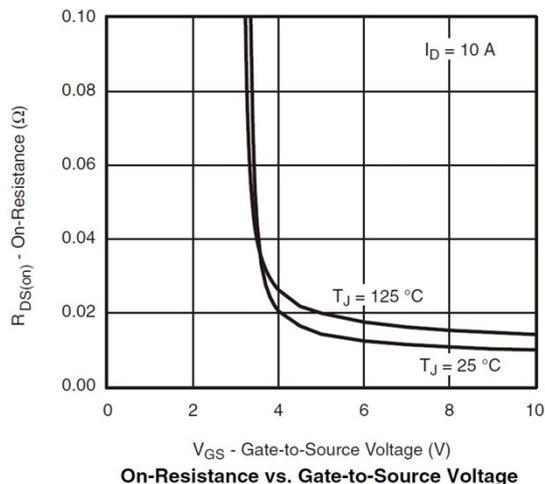
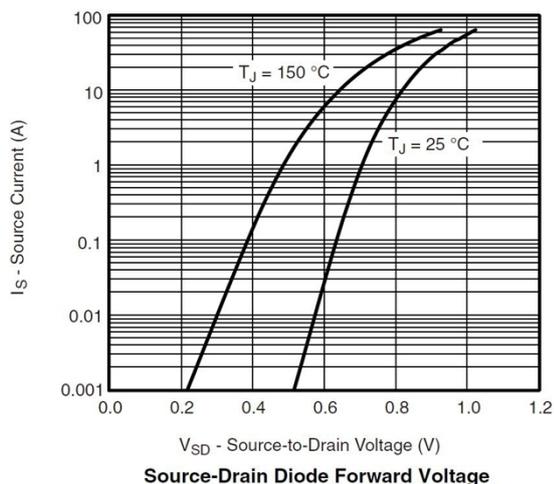
Capacitance

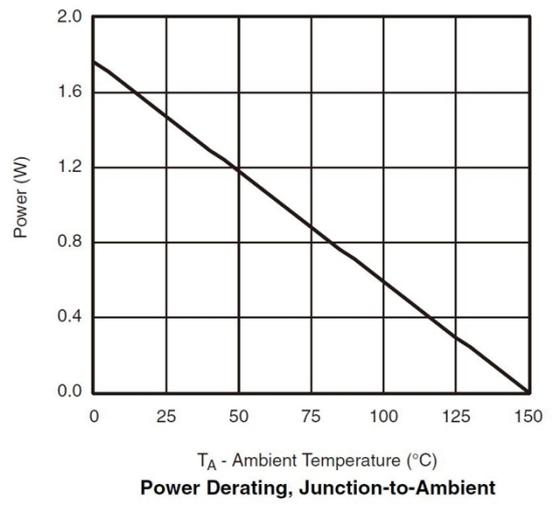
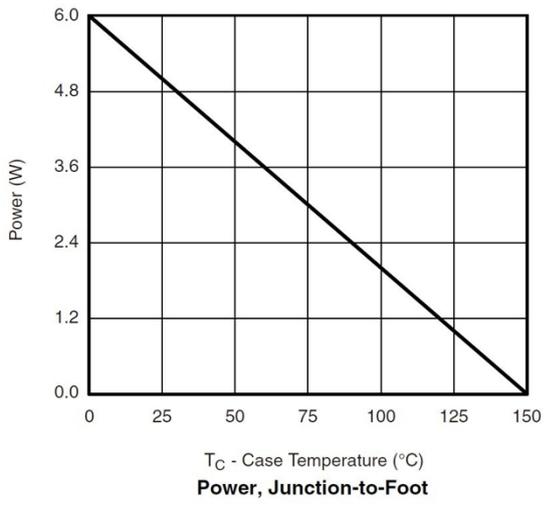


Gate Charge

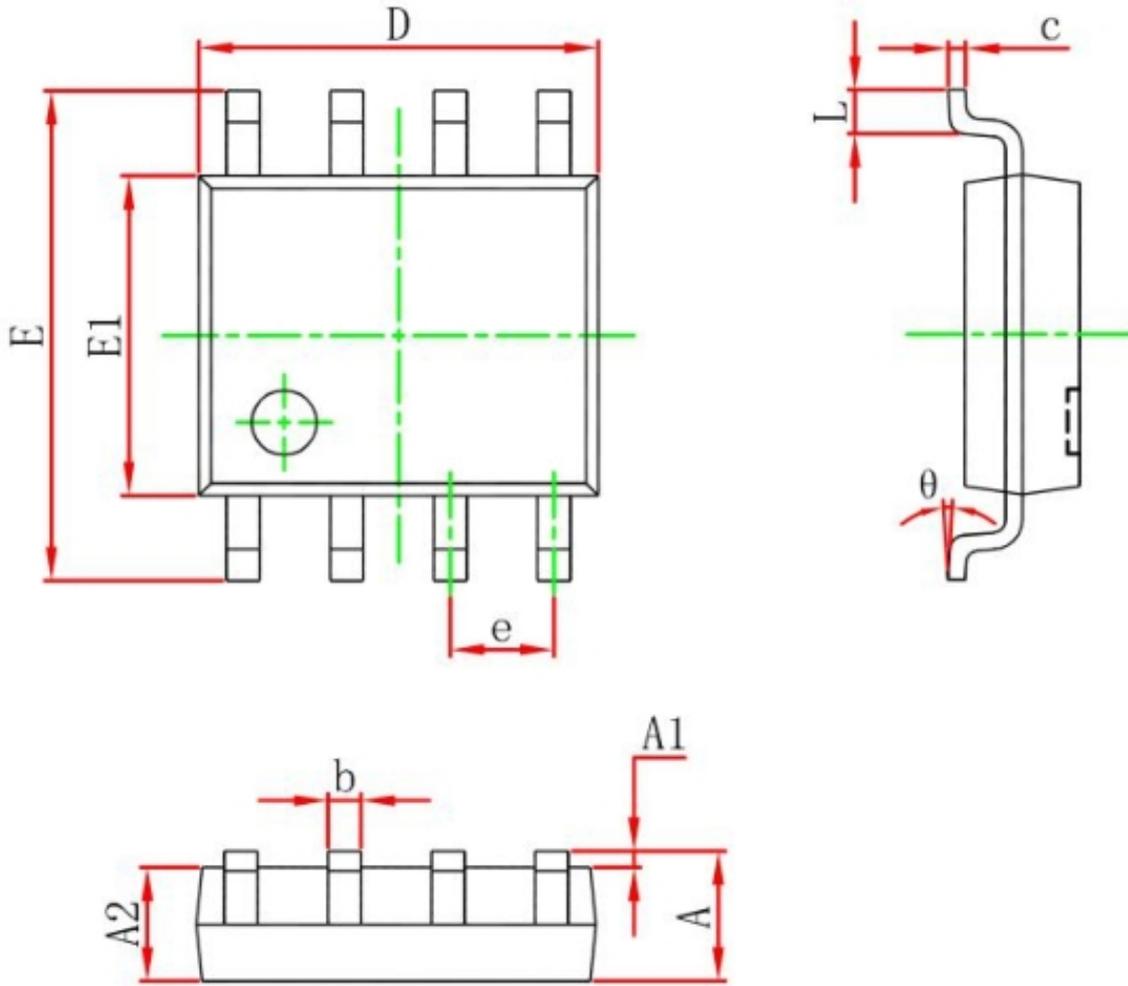


On-Resistance vs. Junction Temperature





SOP-8 Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	1.35	1.75
A1	0.10	0.25
A2	1.35	1.55
b	0.33	0.51
c	0.17	0.25
D	4.80	5.00
e	1.27 REF.	
E	5.80	6.20
E1	3.80	4.00
L	0.40	1.27
θ	0°	8°