

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
30V	20mΩ@10V	6.5A
	25mΩ@4.5V	

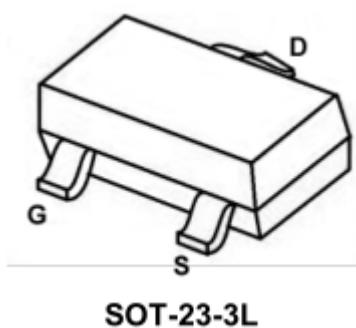
Feature

- TrenchFET Power MOSFET
- Excellent RDS(on) and Low Gate Charge

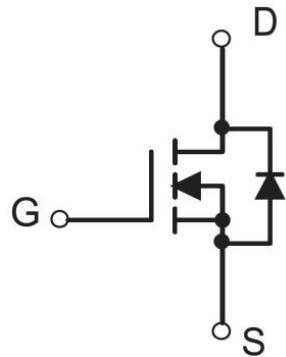
Application

- DC-DC Converter
- Ideal for high-frequency switching and synchronous rectification
- Battery Switch

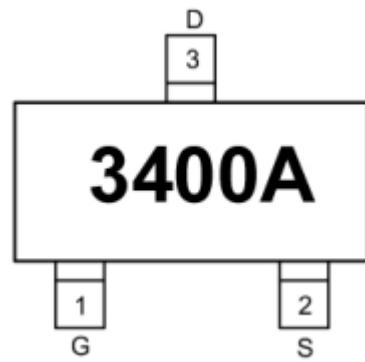
Package



Circuit diagram



Marking



Absolute maximum ratings

($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	6.5	A
Pulsed Drain Current ¹⁾	I_{DM}	26	A
Power Dissipation	P_D	1.4	W
Thermal Resistance from Junction to Ambient ²⁾	$R_{\theta JA}$	89.3	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Electrical characteristics

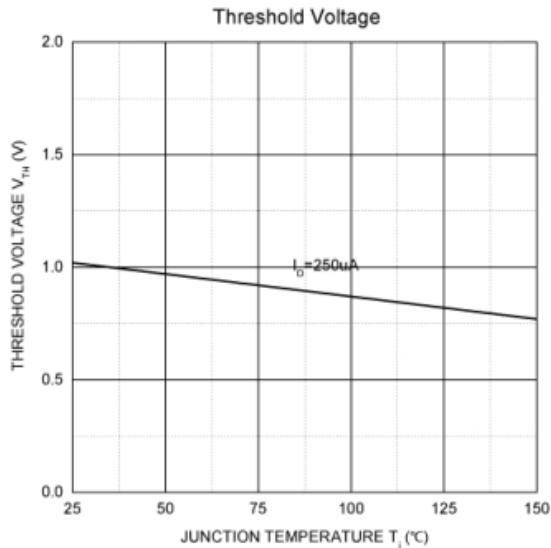
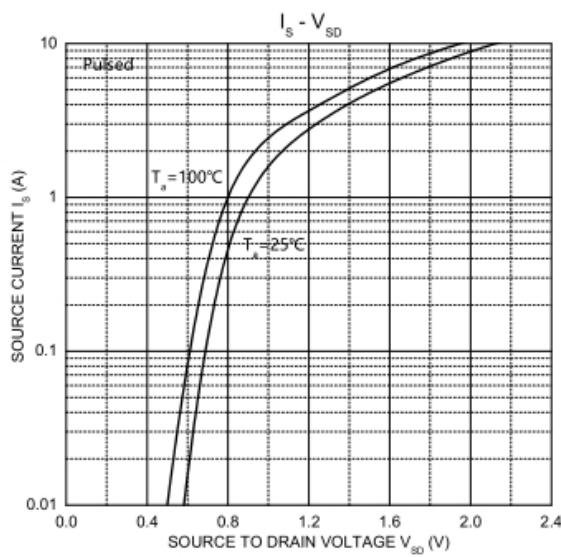
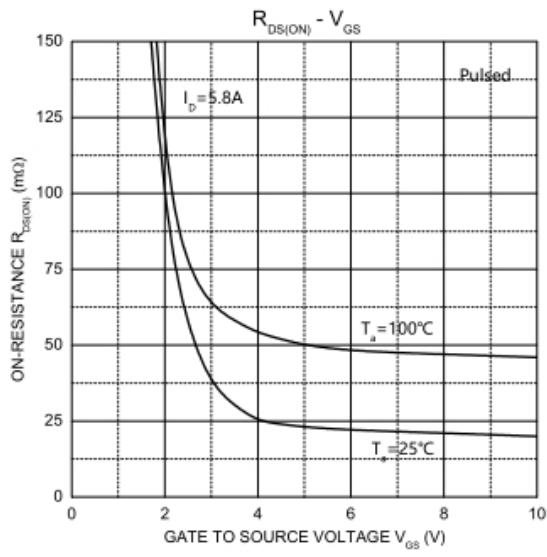
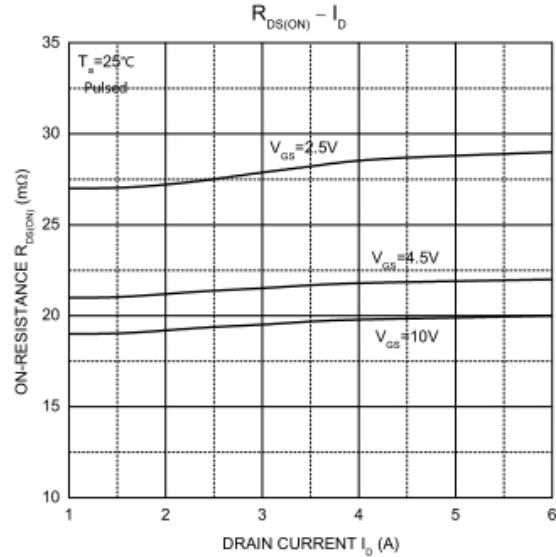
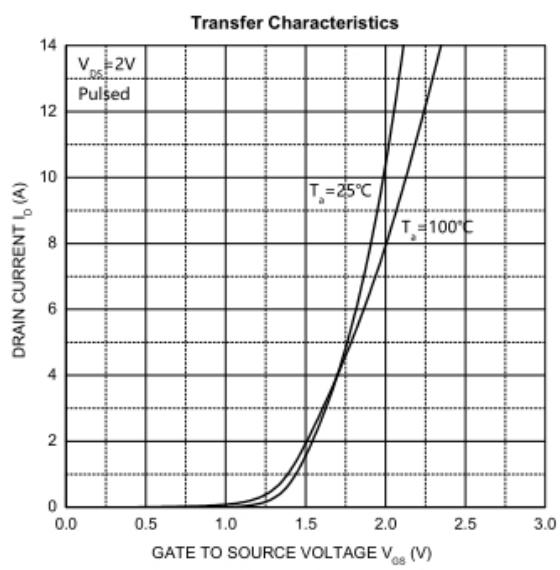
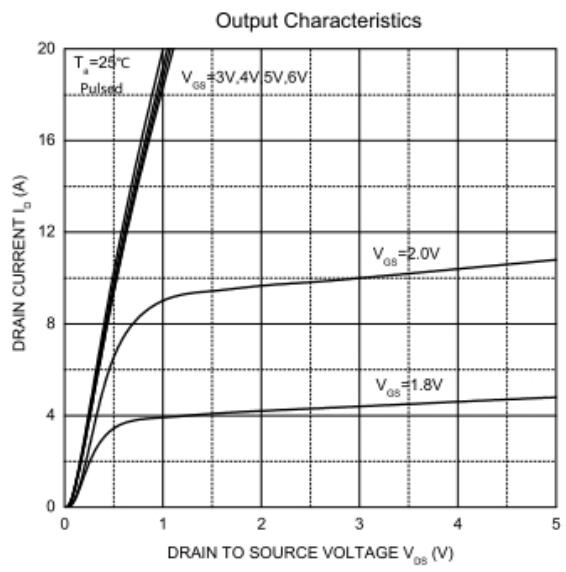
($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$\text{BV}_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 12\text{V}, V_{DS} = 0\text{V}$			± 0.1	μA
Gate threshold voltage ⁽¹⁾	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.5	0.9	1.4	V
Drain-source on-resistance	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 5.8\text{A}$		20	27	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 5\text{A}$		25	32	
		$V_{GS} = 2.5\text{V}, I_D = 4\text{A}$			48	
Dynamic Characteristics⁴⁾						
Input capacitance	C_{iss}	$V_{DS} = 15\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		850		pF
Output capacitance	C_{oss}			108		
Reverse transfer capacitance	C_{rss}			84		
Switching Characteristics⁴⁾						
Total gate charge	Q_g	$V_{DS} = 15\text{V}, V_{GS} = 4.5\text{V}, I_D = 5\text{A}$		9.5		nC
Gate-source charge	Q_{gs}			1.5		
Gate-drain charge	Q_{gd}			3		
Turn-on Delay Time	$T_{d(on)}$	$V_{DD} = 10\text{V}, V_{GS} = 15\text{V}, R_L = 2.7\Omega, R_{GEN} = 3\Omega$		3.5		nS
Turn-on Rise Time	T_r			5		
Turn-Off Delay Time	$T_{d(off)}$			25		
Turn-Off Fall Time	t_f			5		
Source-Drain Diode characteristics						
Diode Forward voltage ⁽³⁾	V_{SD}	$V_{GS} = 0\text{V}, I_S = 1\text{A}$			1.2	V

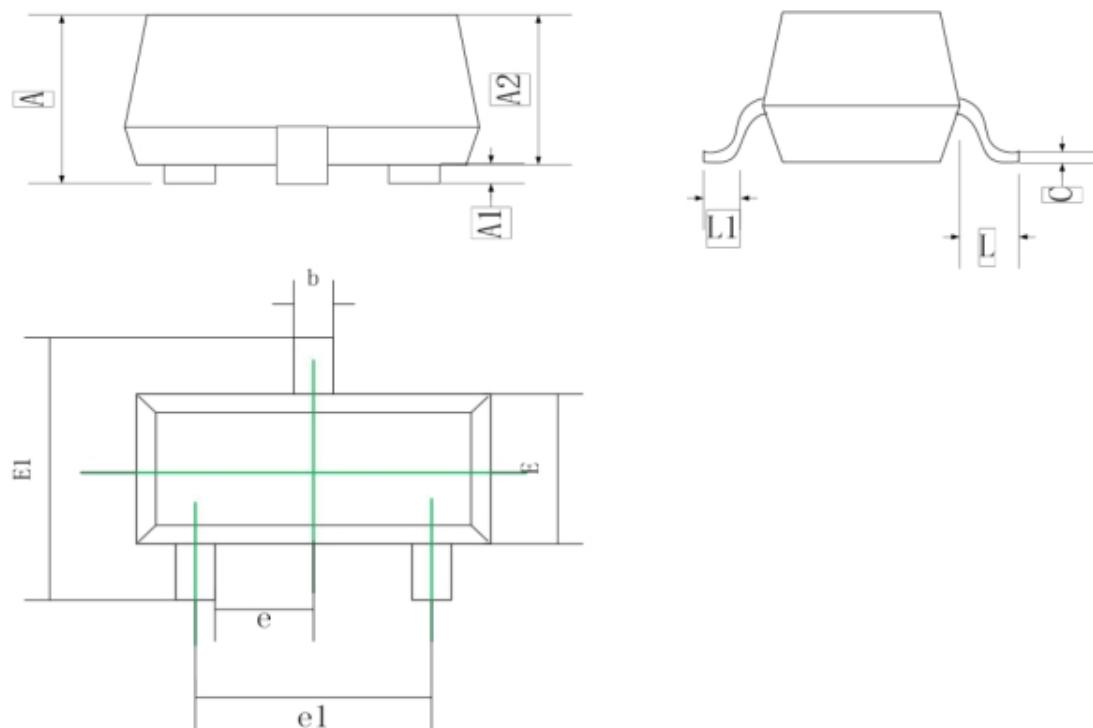
Note :

1. Repetitive Rating : Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t < 5$ sec.
3. Pulse Test : Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production testing.

Typical Characteristics



SOT-23-3L Package Information



Symbol	Dimensions in millimeters	
	Min.	Max.
A	1.050	1.250
A1	0.000	0.100
A2	1.050	1.150
b	0.300	0.500
c	0.100	0.200
D	2.820	3.020
E	1.500	1.700
E1	2.650	2.950
e	0.950 Typ.	
e1	1.800	2.000
L	0.300	0.600
θ	0°	8°