

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
-20V	27mΩ@-4.5V	-6A
	35mΩ@-2.5V	

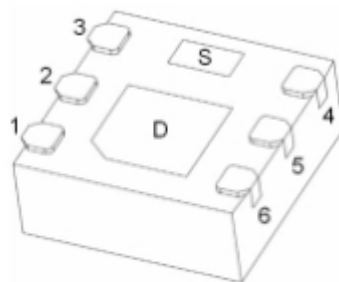
## Feature

- Advanced trench MOSFET process technology
- Ultra low on-resistance with low gate charge

## Applications

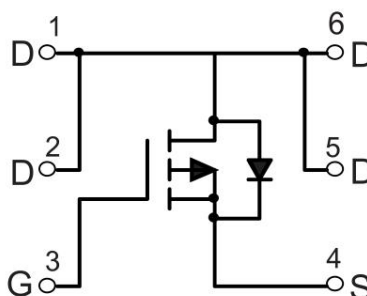
- PWM application
- Load switch
- Battery charge in cellular handset

## Package

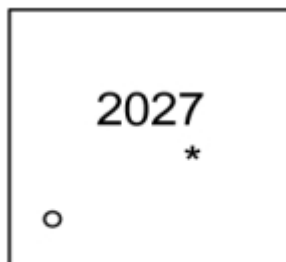


PDFN2X2-6L

## Circuit diagram



## Marking



**2027** =Device Code  
**\*** =Month Code

## Absolute maximum ratings

(T<sub>a</sub>=25°C unless otherwise noted)

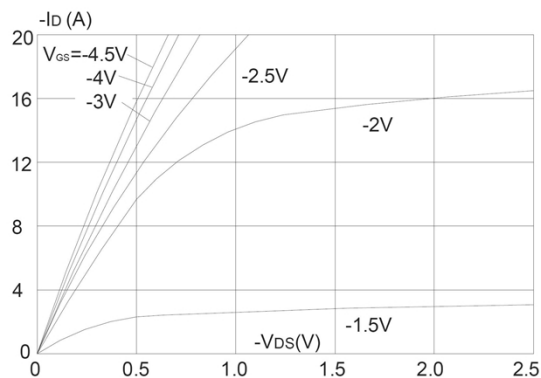
Parameter	Symbol	Value	Unit
Drain-source Voltage	V <sub>DS</sub>	-20	V
Gate-source Voltage	V <sub>GS</sub>	±12	V
Drain Current (T <sub>C</sub> =25°C)	I <sub>D</sub>	-6	A
Pulsed Drain Current <sup>1</sup>	I <sub>DM</sub>	-24	A
Total Power Dissipation (T <sub>C</sub> =25°C)	P <sub>D</sub>	10	W
Thermal Resistance Junction-to-Case @ Steady State	R <sub>θJC</sub>	12.5	°C/ W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 ~ +150	°C

## Electrical characteristics

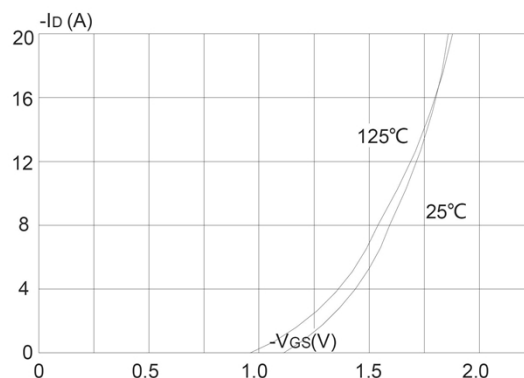
( $T_A=25^{\circ}\text{C}$ , unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	BV (BR)DSS	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-20			V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V			-1	uA
Gate-body leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = ±12V, V <sub>DS</sub> = 0V			±100	uA
Gate threshold voltage <sup>(1)</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.6	-0.9	-1.3	V
Drain-source on-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3A		27	35	mΩ
		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -2A		35	46	
Dynamic Characteristics						
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -10V, V <sub>GS</sub> =0V, f=1MHz		775		pF
Output capacitance	C <sub>oss</sub>			105		
Reverse transfer capacitance	C <sub>rss</sub>			66		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -10V,V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -5A		7.8		nC
Gate-Source Charge	Q <sub>gs</sub>			1.3		
Gate-Drain Charge	Q <sub>gd</sub>			1.6		
Turn-on Delay Time	T <sub>d(on)</sub>	V <sub>DD</sub> = -10V, V <sub>GEN</sub> = -4.5V, I <sub>D</sub> = -4A, R <sub>GEN</sub> =1Ω, R <sub>L</sub> =6Ω		10		nS
Turn-on Rise Time	T <sub>r</sub>			32		
Turn-Off Delay Time	T <sub>d(off)</sub>			50		
Turn-Off Fall Time	t <sub>f</sub>			51		
Source-Drain Diode Characteristics						
Diode Forward voltage	V <sub>DS</sub>	I <sub>S</sub> = -1A, V <sub>GS</sub> = 0V			-1.2	V

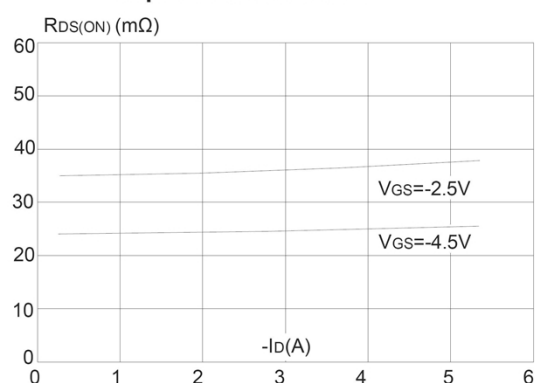
## Typical Characteristics



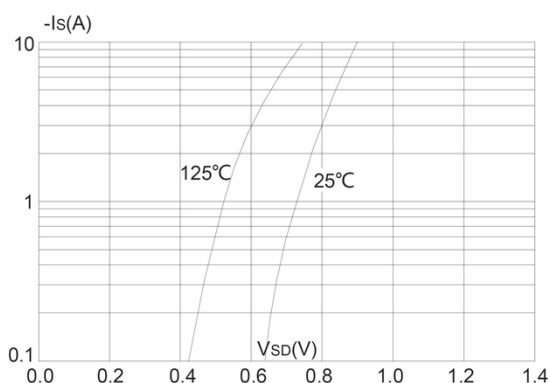
Output Characteristics



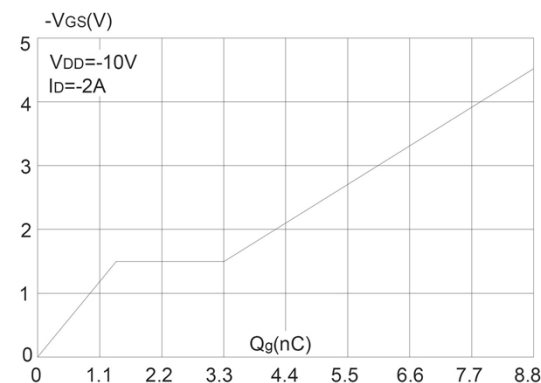
Typical Transfer Characteristics



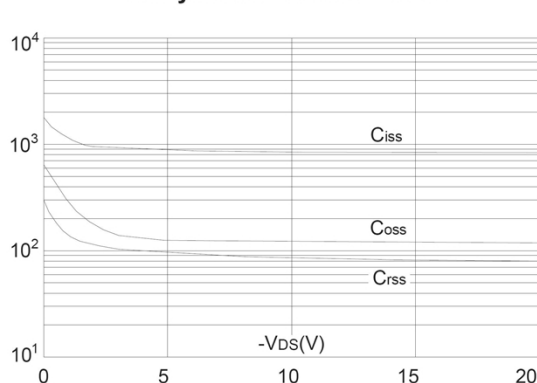
On-resistance vs. Drain Current



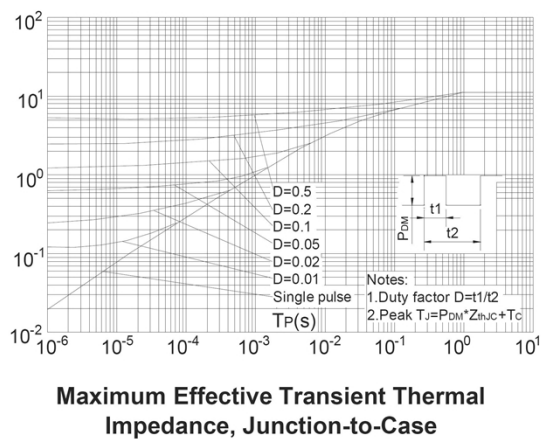
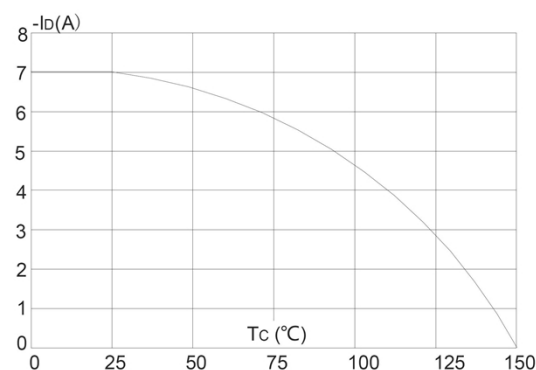
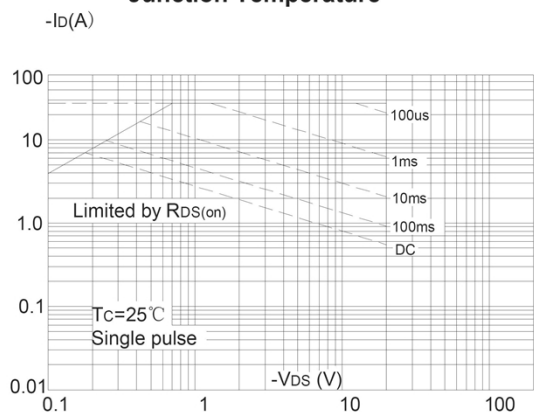
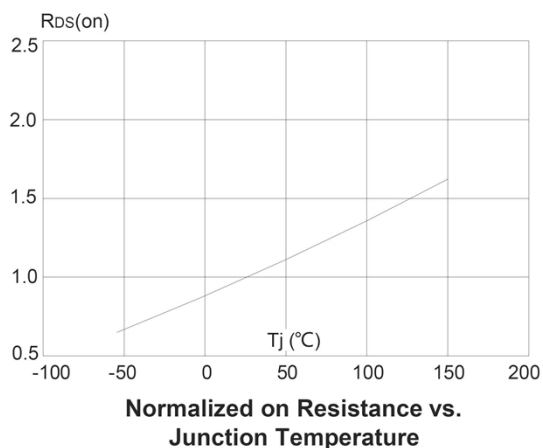
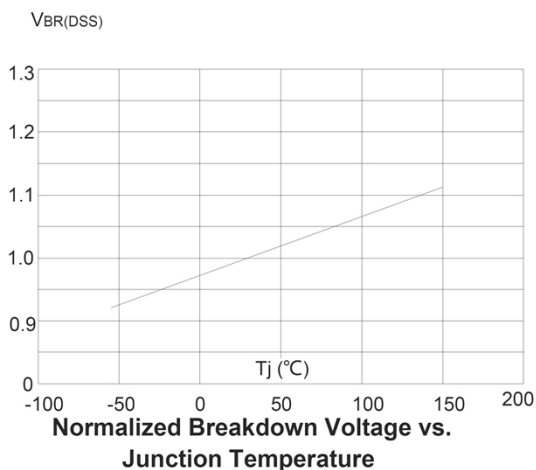
Body Diode Characteristics



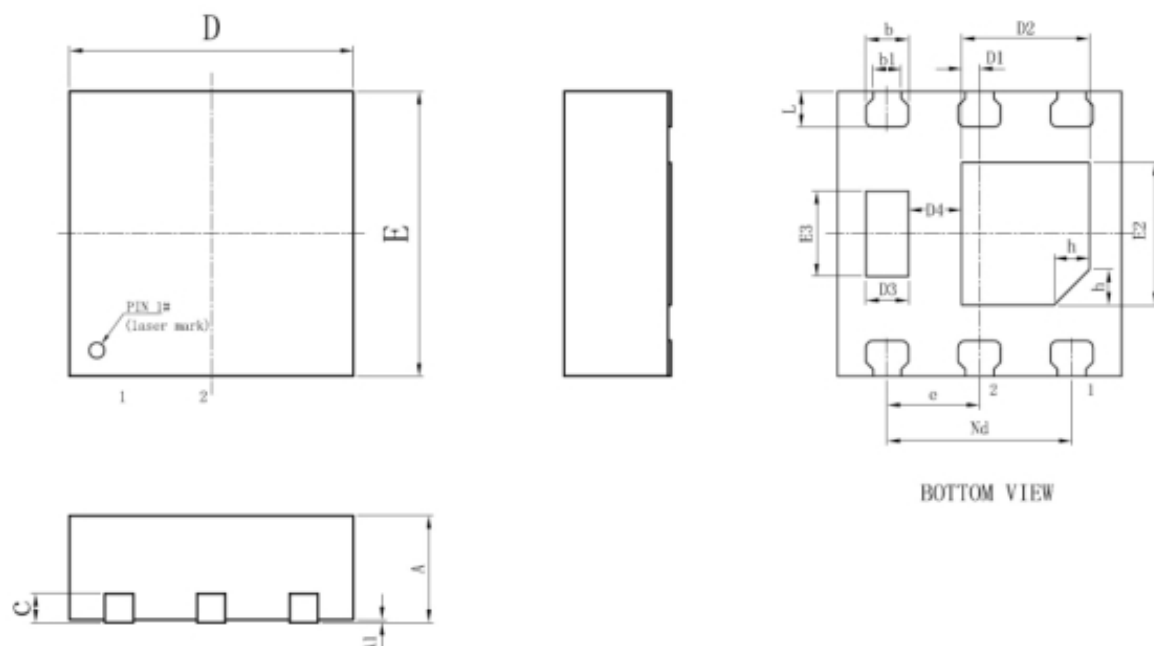
Gate Charge Characteristics



Capacitance Characteristics  
C(pF)



## PDFN2X2-6L Package Information



Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.70	0.75	0.80
A1		0.02	0.05
b	0.25	0.30	0.35
b1	0.20REF		
c	0.203REF		
D	1.90	2.00	2.10
D1	0.08	0.125	0.18
D2	0.85	0.90	0.95
D3	0.25	0.30	0.35
D4	0.33	0.375	0.43
e	0.65BSC		
Nd	1.30BSC		
E	1.90	2.00	2.10
E2	0.95	1.00	1.05
E3	0.55	0.60	0.65
L	0.20	0.25	0.30
h	0.25REF		