

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
-20V	0.65Ω@-4.5V	-0.66A
	0.85Ω@-2.5V	

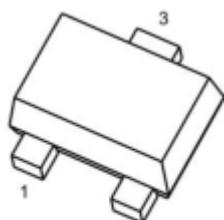
## Feature

- Surface Mount Package
- P-Channel Switch with Low RDS(on)
- Operated at Low Logic Level Gate Drive
- ESD Protected: 2KV

## Applications

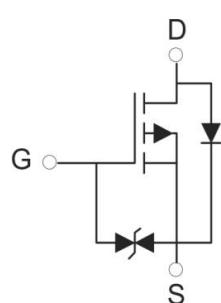
- Load/Power Switching
- Interfacing Switching
- Battery Management for Ultra Small Portable Electronics

## Package

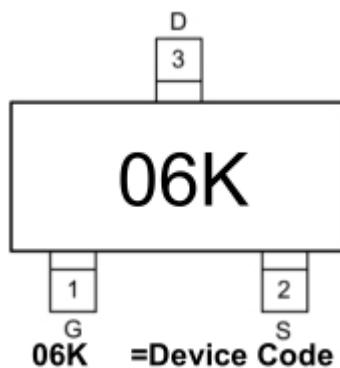


**SOT-723**

## Circuit diagram



## Marking



## Absolute maximum ratings

( $T_a=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current	$I_D$	-0.66	A
Pulsed Drain Current	$I_{DM}$	-1.2	A
Power Dissipation	$P_D$	0.15	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	833	$^\circ\text{C}/\text{W}$
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature	$T_{STG.,}$	-55~+150	$^\circ\text{C}$

## Electrical characteristics

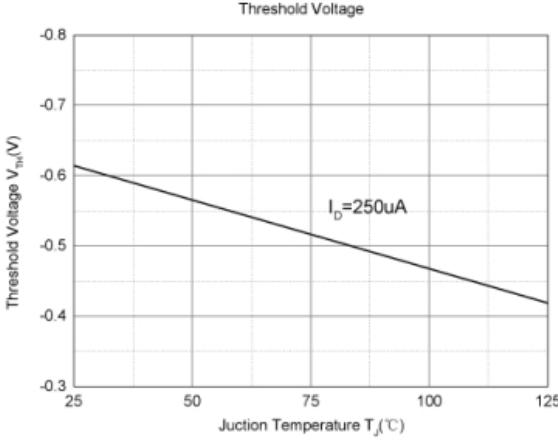
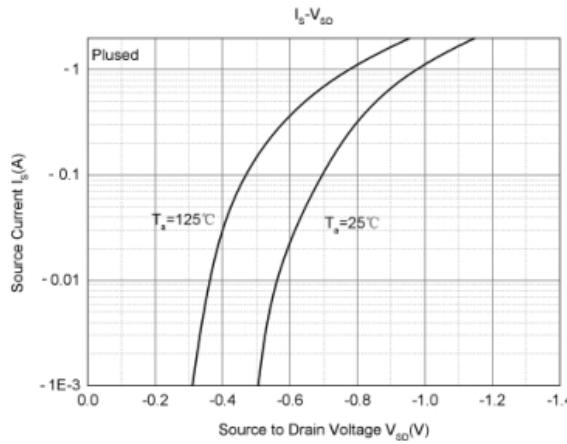
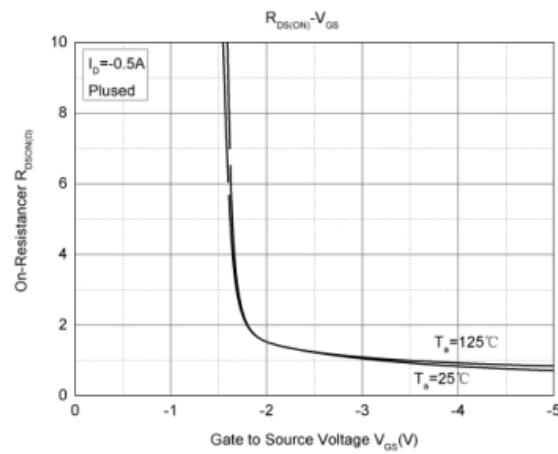
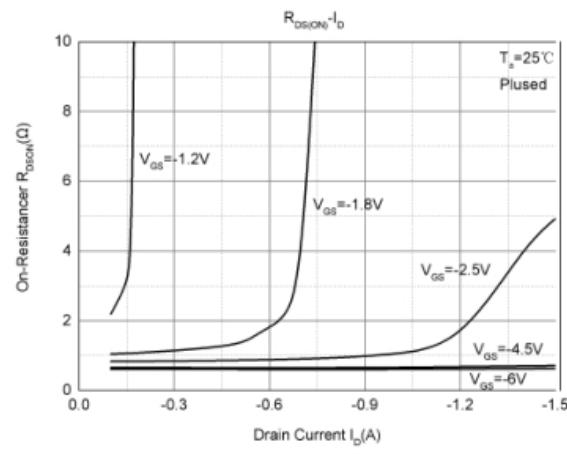
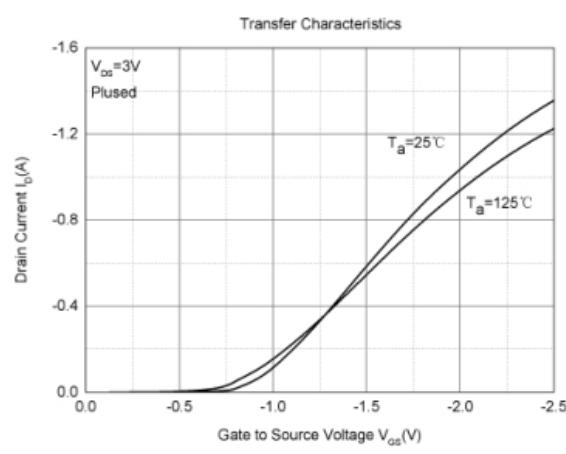
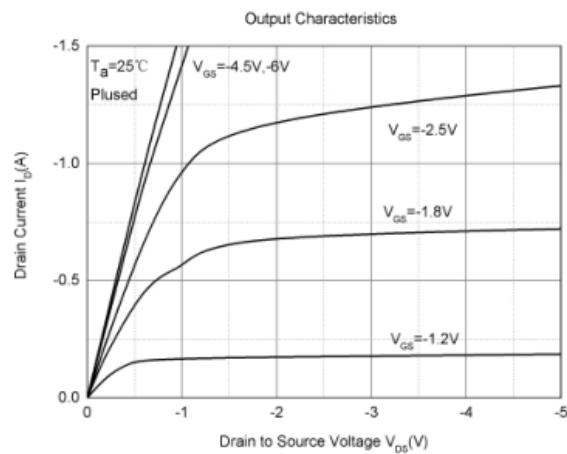
( $T_A=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	$\text{BV}_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$	-20			V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = -16\text{V}, V_{GS} = 0\text{V}$			-1	$\mu\text{A}$
Gate-body leakage current	$I_{GSS}$	$V_{GS} = \pm 10\text{V}, V_{DS} = 0\text{V}$			$\pm 10$	$\mu\text{A}$
Gate threshold voltage <sup>(1)</sup>	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-0.35	-0.65	-1	V
Drain-source on-resistance <sup>(1)</sup>	$R_{DS(\text{on})}$	$V_{GS} = -4.5\text{V}, I_D = -0.5\text{A}$		0.65	0.75	$\Omega$
		$V_{GS} = -2.5\text{V}, I_D = -0.2\text{A}$		0.85	1.0	
<b>Dynamic Characteristics</b>						
Input capacitance	$C_{iss}$	$V_{DS} = -16\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		113		$\text{pF}$
Output capacitance	$C_{oss}$			15		
Reverse transfer capacitance	$C_{rss}$			9		
Turn-on Delay Time	$T_{d(on)}$	$V_{DS} = -10\text{V}, I_D = -200\text{mA}, V_{GS} = -4.5\text{V}, R_{GEN} = 10\Omega$		9		$\text{nS}$
Turn-on Rise Time	$T_r$			5.7		
Turn-Off Delay Time	$T_{d(off)}$			32.6		
Turn-Off Fall Time	$t_f$			20.3		
<b>Source-Drain Diode Characteristics</b>						
Diode Forward voltage	$V_{SD}$	$I_S = -0.5\text{A}, V_{GS} = 0\text{V}$			-1.2	V

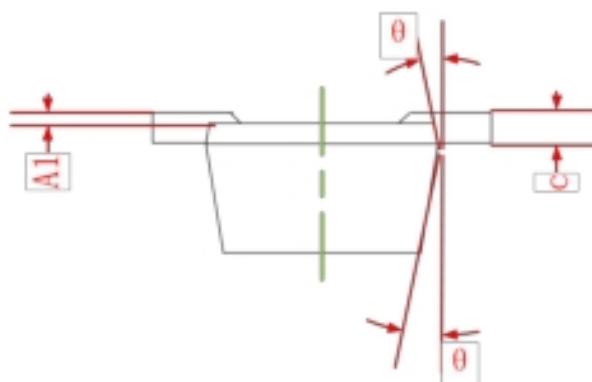
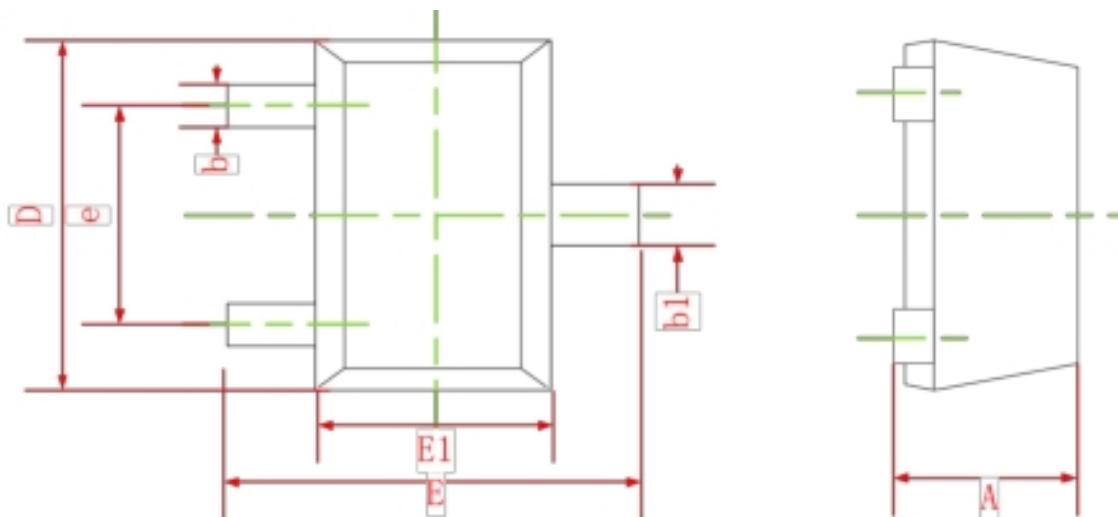
### Notes:

1. Pulse Test: Pulse Width < 300μs, Duty Cycle ≤ 2%.
2. Guaranteed by design, not subject to production testing.

## Typical Characteristics



## SOT-723 Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	0.430	0.500
A1	0.000	0.050
b	0.170	0.270
b1	0.270	0.370
c	0.080	0.150
D	1.150	1.250
E	1.150	1.250
E1	0.750	0.850
e	0.800TYP.	
θ	7° REF.	