

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
40V	2.9mΩ@10V	75A
	3.7mΩ@4.5V	

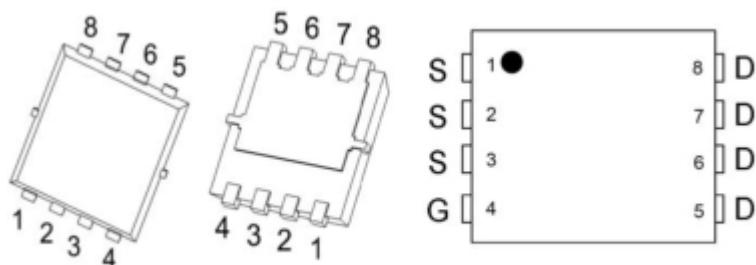
Feature

- Fast Switching
- Low Gate Charge and Rdson
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test

Application

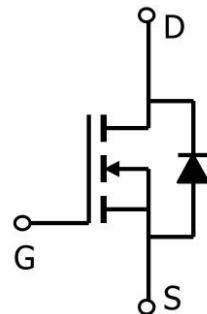
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

Package

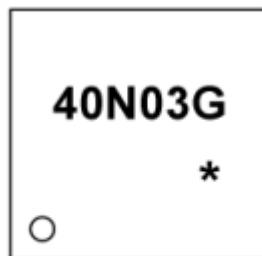


PDFNWB3.3×3.3-8L

Circuit diagram



Marking



40N03G = Device Code
 * = Month Code

Absolute maximum ratings

($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹ ($T_c=25^\circ\text{C}$)	I_D	55	A
Pulsed Drain Current ²	I_{DM}	220	A
Single Pulse Avalanche Energy ³	E_{AS}	256	mJ
Avalanche Current	I_{AS}	32	A
Total Power Dissipation ⁴	P_D	65	W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	1.92	$^\circ\text{C}/\text{W}$
Storage Temperature Range	T_{STG}	-55 to 175	$^\circ\text{C}$
Operating Junction Temperature Range	T_J	-55 to 175	$^\circ\text{C}$



Electrical characteristics

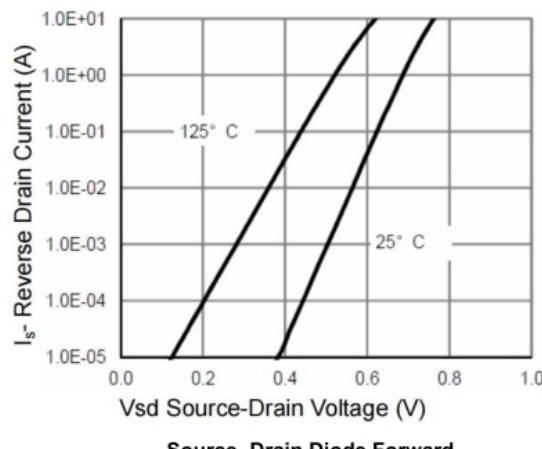
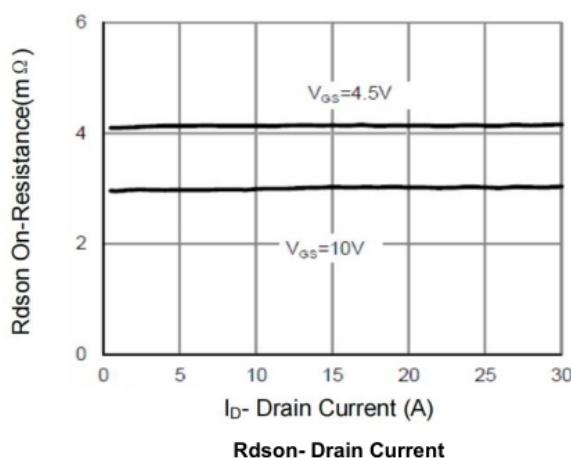
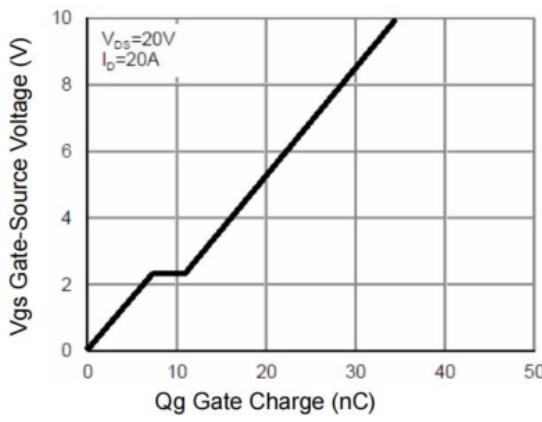
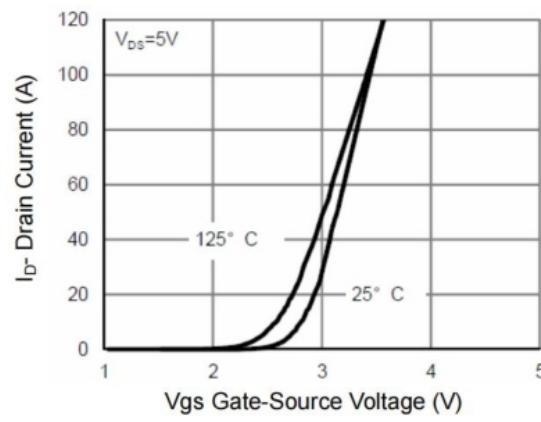
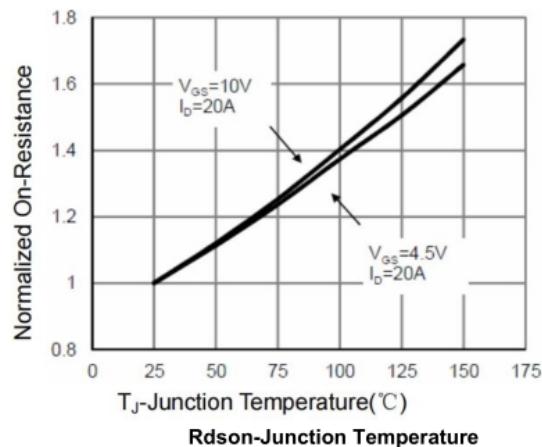
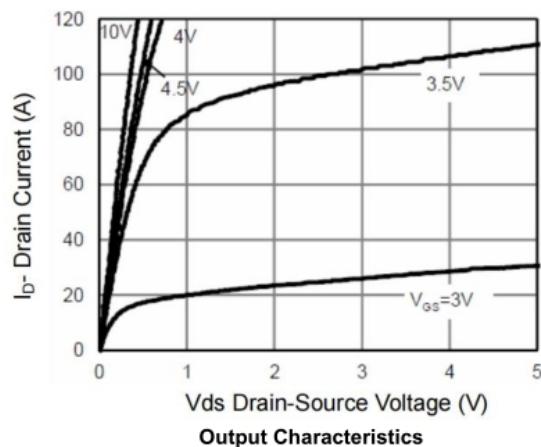
($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$\text{BV}_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	40			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$			± 100	μA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.5	2.5	V
Static Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 30\text{A}$		2.9	3.7	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 20\text{A}$		4.2	5.6	
Dynamic Characteristics						
Total Gate Charge	Q_g	$V_{DS} = 20\text{V}, I_D = 20\text{A}, V_{GS} = 10\text{V}$		35		pF
Gate-Source Charge	Q_{gs}			5.1		
Gate-Drain Charge	Q_{gd}			4		
Input Capacitance	C_{iss}	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		1687		pF
Output Capacitance	C_{oss}			641		
Reverse Transfer Capacitance	C_{rss}			32		
Switching Characteristics						
Turn-On Delay Time	$T_{d(on)}$	$V_{DD} = 20\text{V}, V_{GS} = 10\text{V}, R_G = 1.6\Omega, I_D = 20\text{A}$		7.8		nS
Rise Time	T_r			4.2		
Turn-Off Delay Time	$T_{d(off)}$			28		
Fall Time	T_f			3.8		
Diode Characteristics						
Diode Forward Voltage2	V_{SD}	$V_{GS} = 0\text{V}, I_S = 1\text{A}, T_J = 25^\circ\text{C}$			1.2	V

Note:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating. The test condition is $V_{DD} = 20\text{V}, V_{GS} = 10\text{V}, L = 0.5\text{mH}, R_G = 25\Omega$
4. The power dissipation is limited by 150°C junction temperature

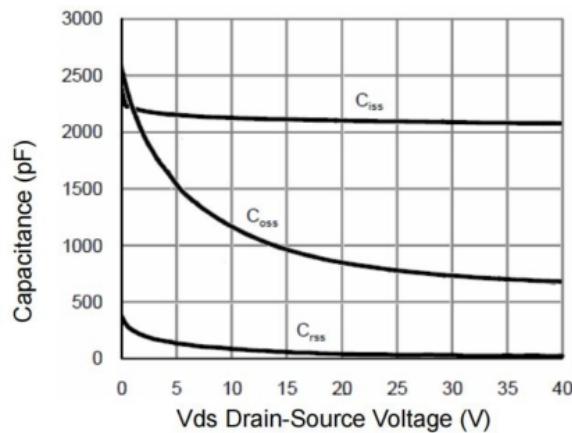
Typical Characteristics



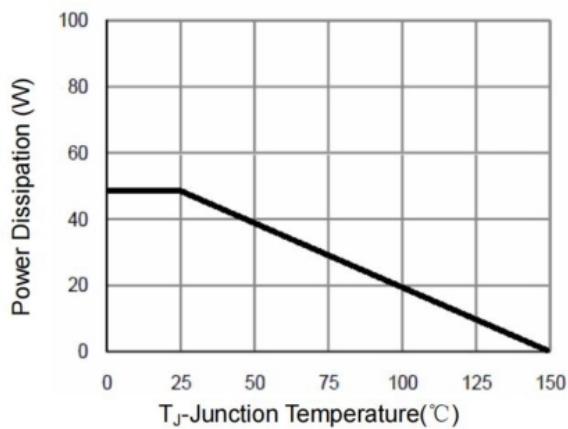


ZL MOSFET

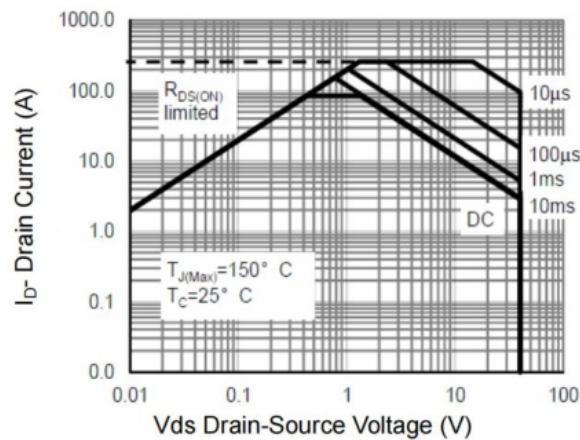
ZL40N03G



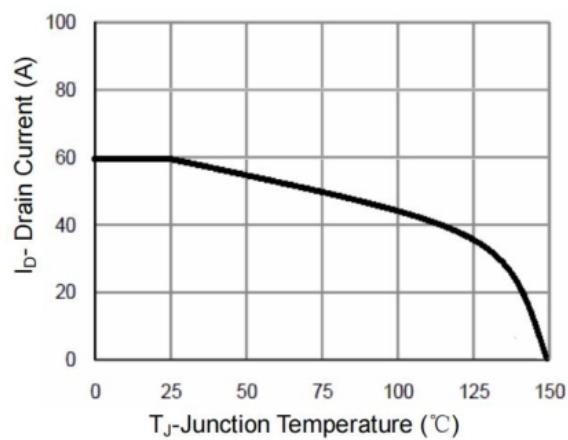
Capacitance vs Vds



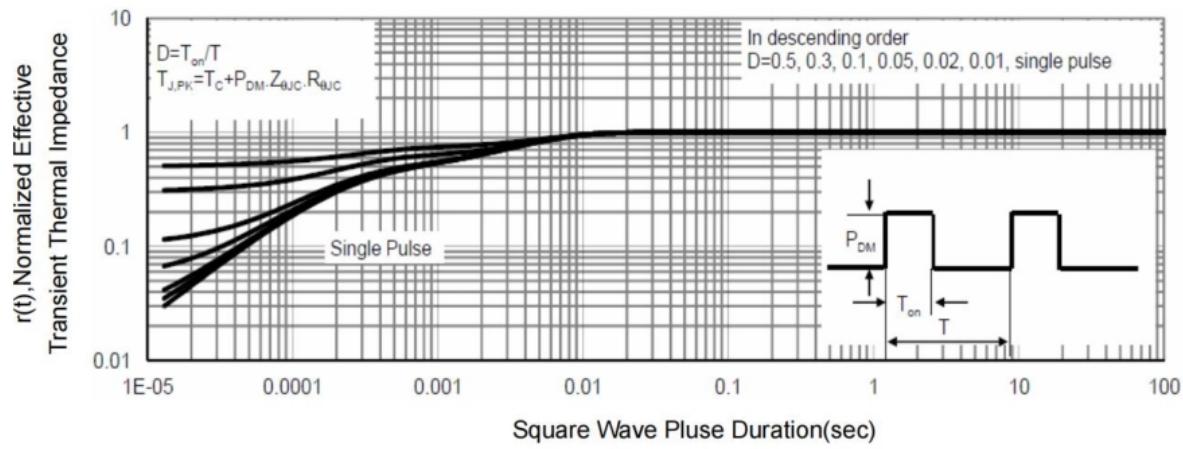
Power De-rating



Safe Operation Area

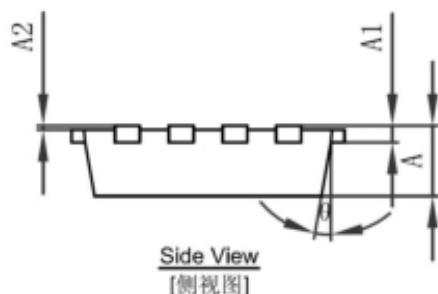
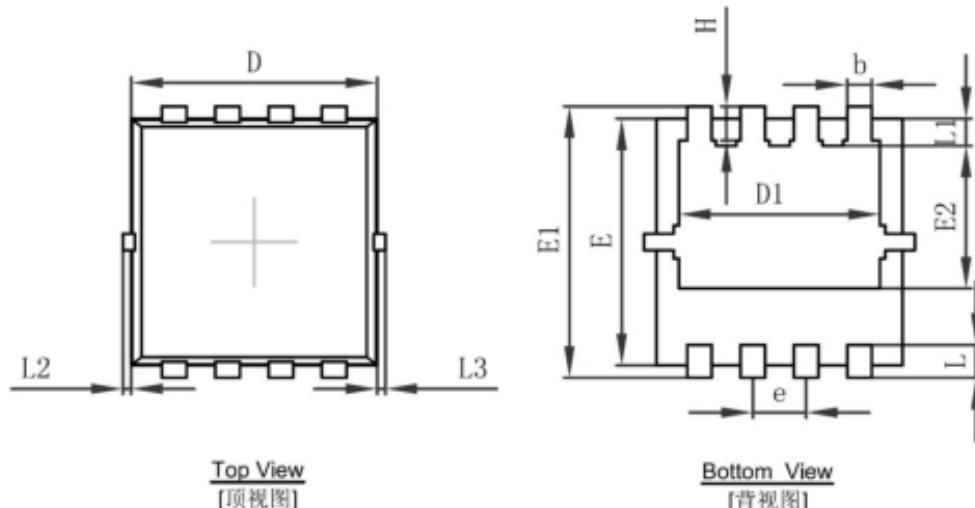


Current De-rating



Normalized Maximum Transient Thermal Impedance

PDFNWB3.3×3.3-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.650	0.850	0.026	0.033
A1	0.152 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	2.900	3.100	0.114	0.122
D1	2.300	2.600	0.091	0.102
E	2.900	3.100	0.114	0.122
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0~0.100		0~0.004	
L3	0~0.100		0~0.004	
H	0.315	0.515	0.012	0.020
θ	9°	13°	9°	13°