

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
-30V	3mΩ@-10V	-90A
	4mΩ@-4.5V	

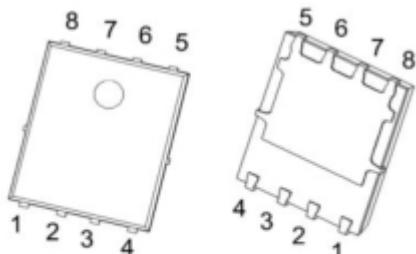
Feature

- High switching speed
- Low Gate Charge
- High density cell design for ultra low Rdson
- 100% Single Pulse avalanche energy Test

Application

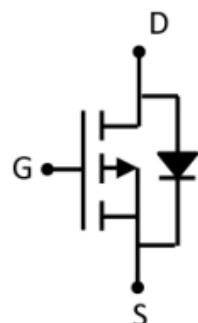
- Load Switching
- DC-DC

Package

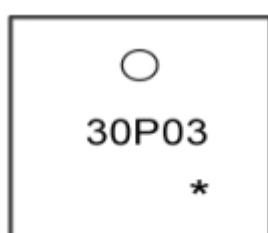


PDFNWB5X6-8L

Circuit diagram



Marking



**30P03 =Device Code
* =Month Code**

Absolute maximum ratings

($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ($T_c=25^\circ\text{C}$)	I_D	-90	A
Pulsed Drain Current	I_{DM}	-360	A
Single Pulse Avalanche Energy ¹	E_{AS}	306	mJ
Maximum Power Dissipation($T_c=25^\circ\text{C}$)	P_D	80	W
Thermal Resistance Junction-Case	$R_{\theta JC}$	1.56	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~+150	$^\circ\text{C}$

Electrical characteristics

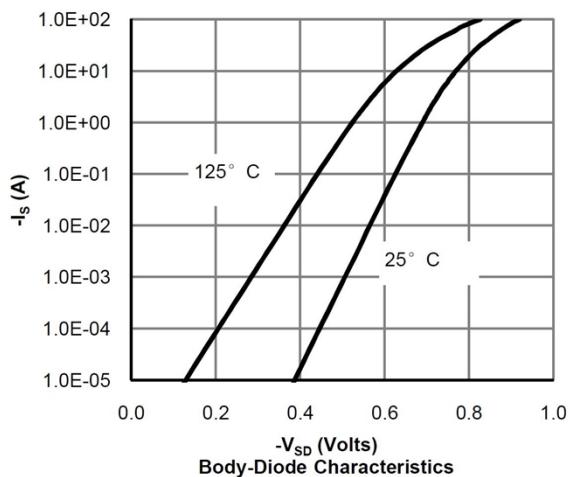
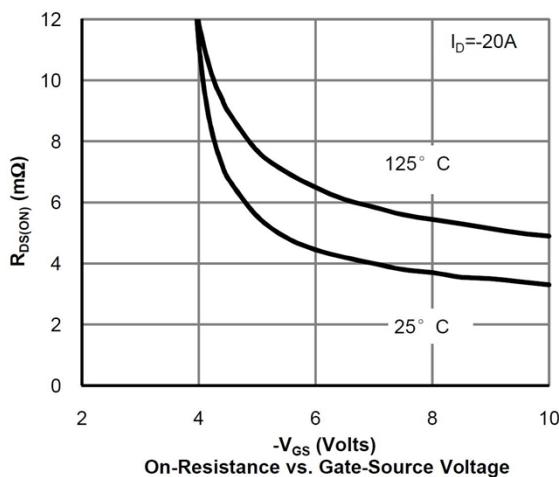
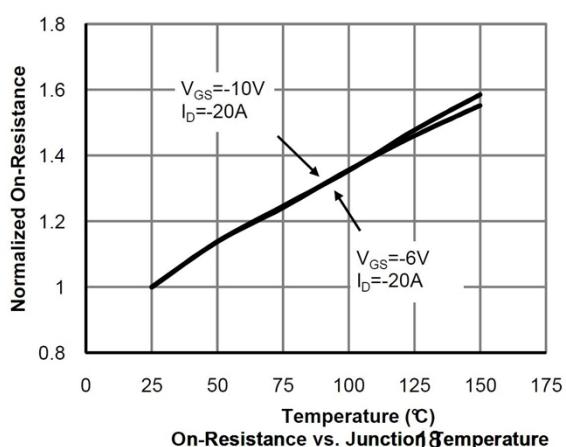
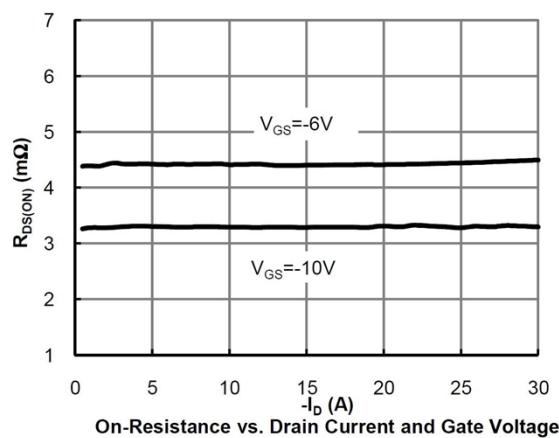
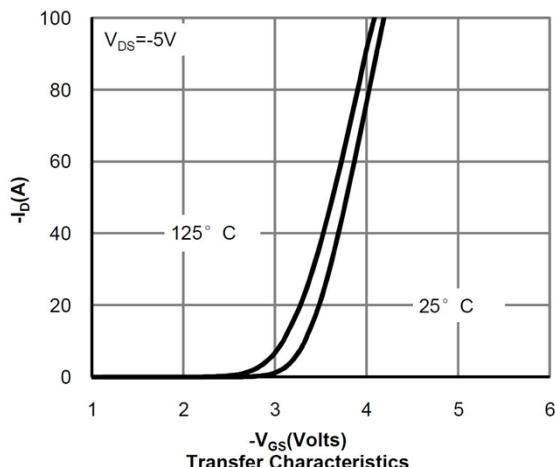
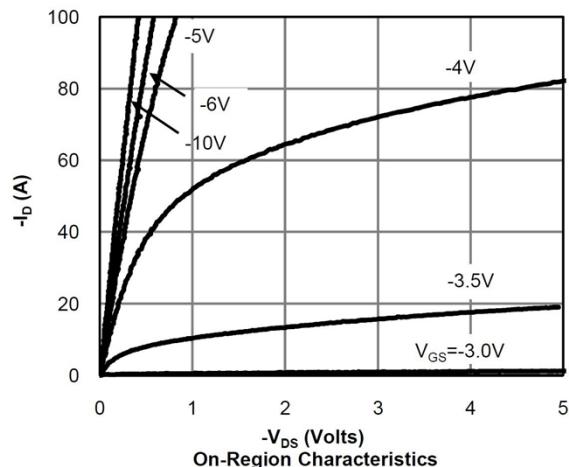
($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$\text{BV}_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24\text{V}, V_{GS} = 0\text{V}$	-	-	-1	μA
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	± 100	μA
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1	-1.6	-2.5	V
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS} = -10\text{V}, I_D = -30\text{A}$	-	3	4	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}, I_D = -20\text{A}$	-	4	6	
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = -15\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		5700		pF
Output Capacitance	C_{oss}			859		
Reverse Transfer Capacitance	C_{rss}			650		
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DD} = -15\text{V}, V_{GS} = -10\text{V}, I_D = 20\text{A}$		75		nC
Gate-Source Charge	Q_{gs}			13		
Gate-Drain Charge	Q_{gd}			23		
Turn-on Delay Time	$T_{d(on)}$	$V_{DD} = -15\text{V}, R_L = 0.75\Omega, V_{GEN} = -10\text{V}, R_{GEN} = 3\Omega$		14		nS
Turn-on Rise Time	T_r			16		
Turn-off Delay Time	$T_{d(off)}$			94		
Turn-off Fall Time	T_f			75		
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{V}, I_s = -1\text{A}$			-1	V

Note:

- The E_{AS} data shows Max. rating . The test condition is $V_{DD} = -15\text{V}, V_{GS} = -10\text{V}, L = 0.5\text{mH}, R_g = 25\Omega$

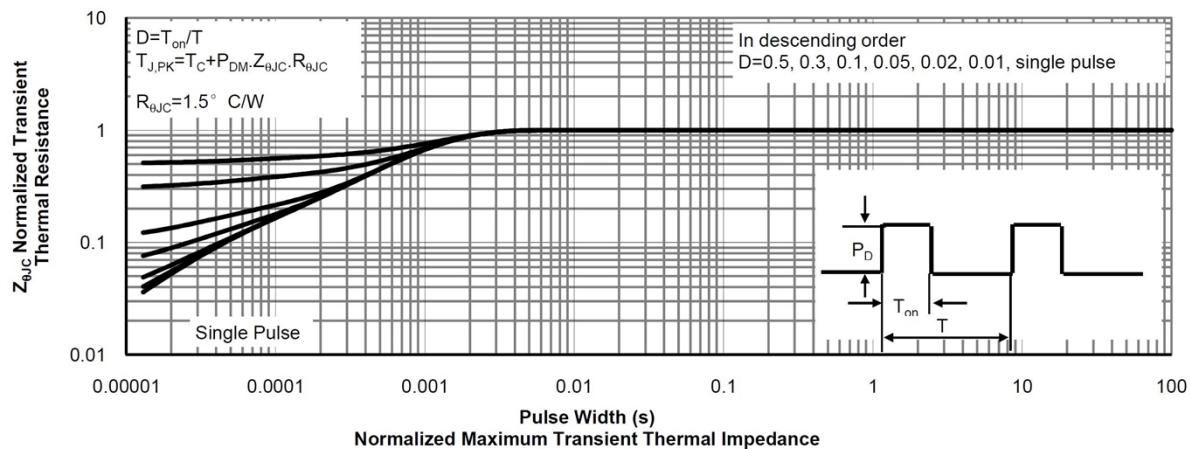
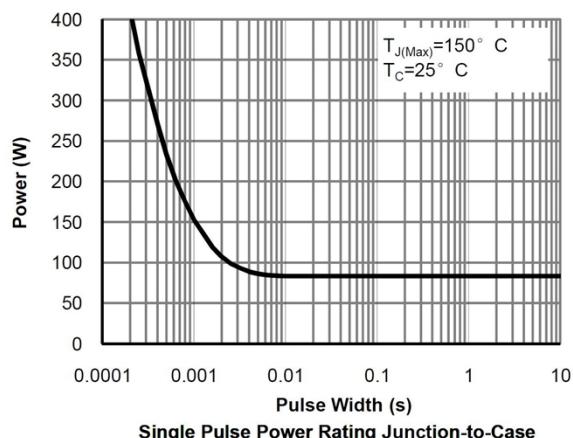
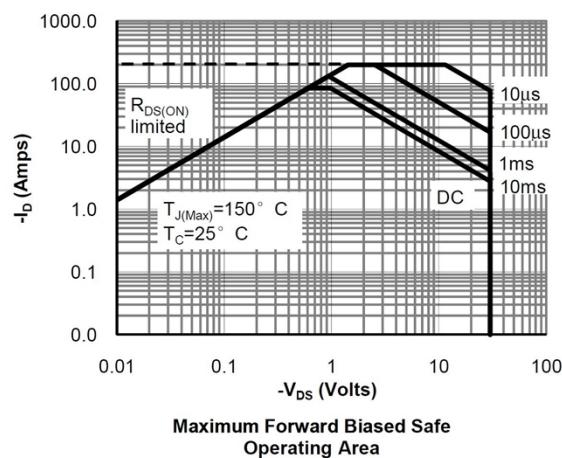
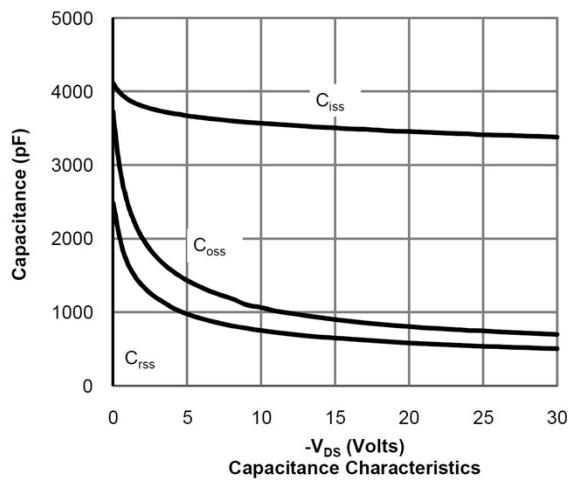
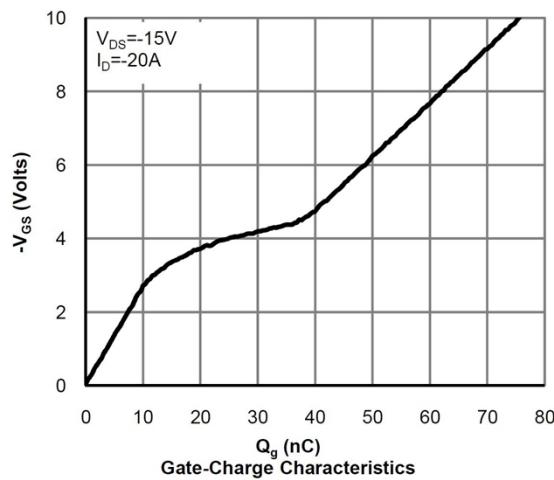
Typical Characteristics



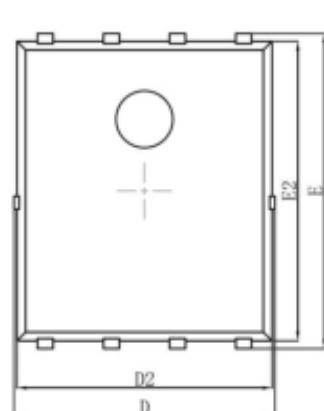


ZL MOSFET

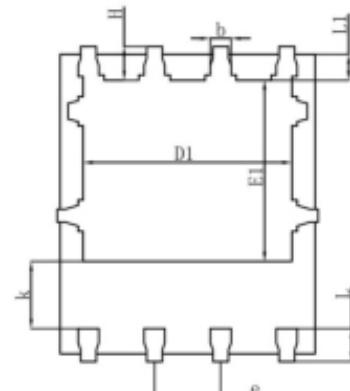
ZL30P03



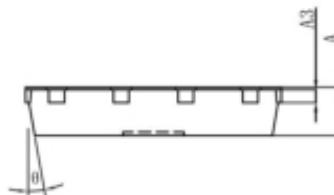
PDFNWB5X6-8L Package Information



Top View
[顶视图]



Bottom View
[背视图]



Side View
[侧视图]

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°