

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
20V	3mΩ@10V	60A
	3.5mΩ@4.5V	

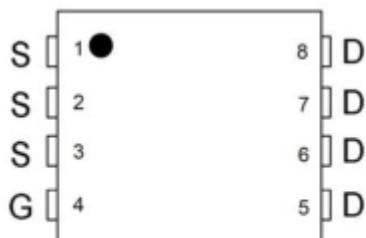
Feature

- $V_{DS} = 20V, I_D = 50A$
- $R_{DS(ON)} < 4.5m\Omega$ @ $V_{GS} = 10V$
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Applications

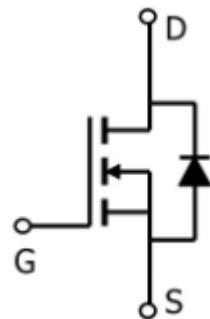
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

Package

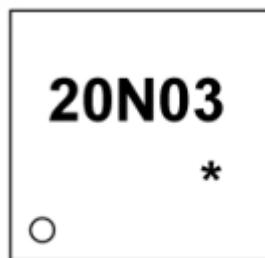


PDFNWB3.3×3.3-8L

Circuit diagram



Marking



**20N03 =Device Code
* =Month Code**

Absolute maximum ratings

($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 10	V
Drain Current-Continuous $ T_C = 25^\circ\text{C}$	I_D	60	W
Pulsed Drain Current	I_{DM}	240	A
Single Pulse Avalanche Energy	E_{AS}	40	mJ
Power Dissipation $ T_C = 25^\circ\text{C}$	P_D	83	W
Thermal Resistance Junction- Case	$R_{\theta JC}$	1.47	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55~+175	$^\circ\text{C}$

Electrical characteristics

($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 10\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	μA
Gate threshold voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.4	0.62	1	V
Static Drain-Source On-Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 10\text{A}$		3	4.5	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_D = 6\text{A}$		3.5	5.5	
Dynamic characteristics⁴						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		3935		pF
Output Capacitance	C_{oss}			701		
Reverse Transfer Capacitance	C_{rss}			333		
Switching Characteristics						
Total Gate Charge(4.5V)	Q_g	$V_{\text{GS}} = 4.5\text{V}, V_{\text{DS}} = 10\text{V}, I_D = 15\text{A}$		105		nC
Gate-Source Charge	Q_{gs}			25		
Gate-Drain Charge	Q_{gd}			21		
Turn-On Delay Time	$T_{\text{d(on)}}$	$V_{\text{GS}} = 4.5\text{V}, V_{\text{DD}} = 10\text{V}, I_D = 10\text{A}, R_L = 1\Omega, R_{\text{GEN}} = 3\Omega$		12		nS
Rise Time	T_r			26		
Turn-Off Delay Time	$T_{\text{d(off)}}$			35		
Fall Time	T_f			10		
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	$I_s = 20\text{A}, V_{\text{GS}} = 0\text{V}$		0.8	1.2	V
Maximum Body-Diode Continuous Current	I_s				60	A

Typical Characteristics

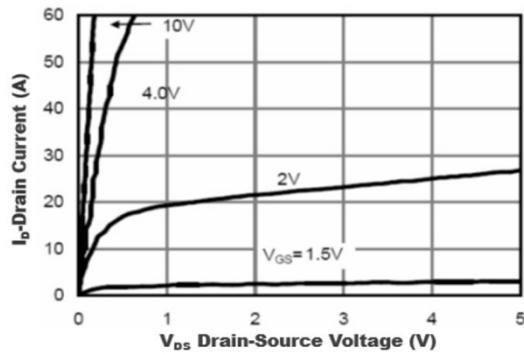


Figure1. Output Characteristics

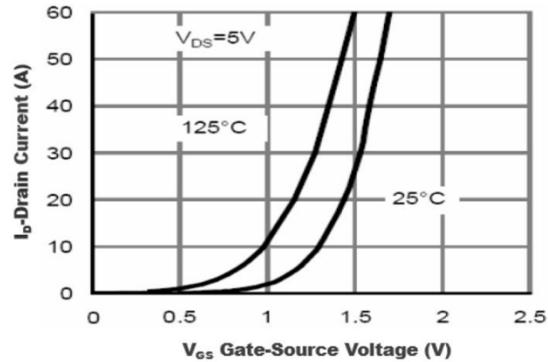


Figure2. Transfer Characteristics

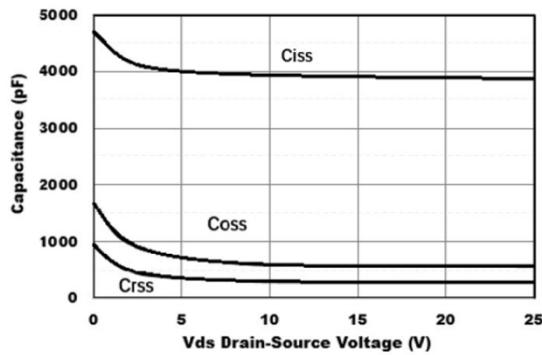


Figure3. Capacitance Characteristics

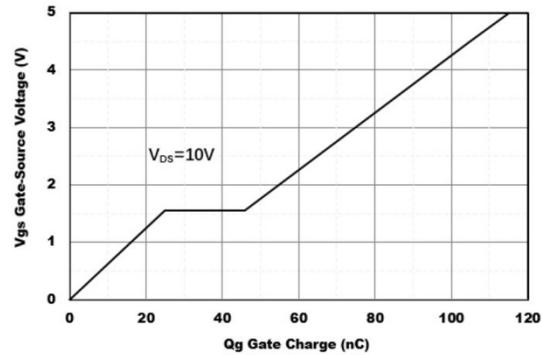


Figure4. Gate Charge

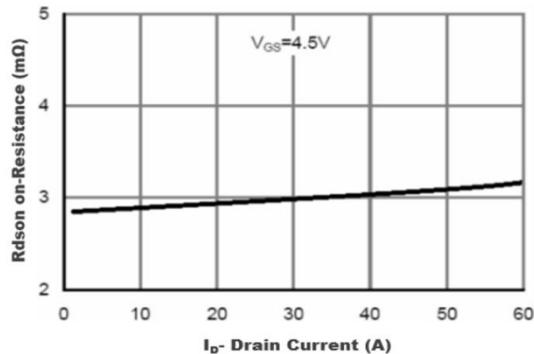


Figure5. Drain-Source on Resistance

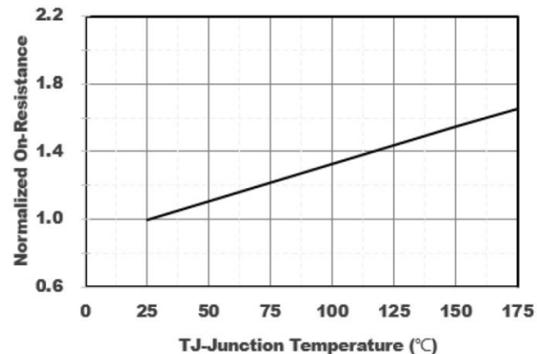


Figure6. Drain-Source on Resistance

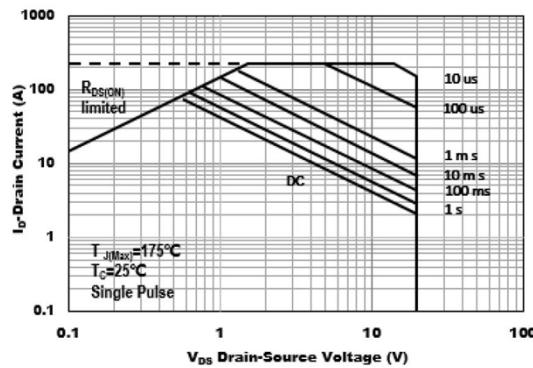


Figure 7. Safe Operation Area

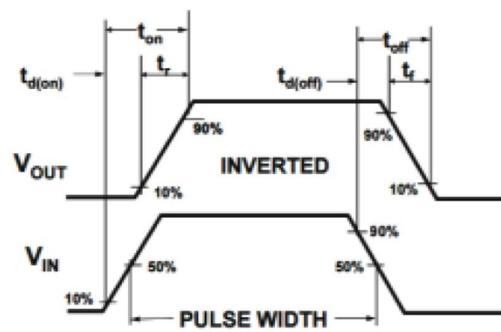
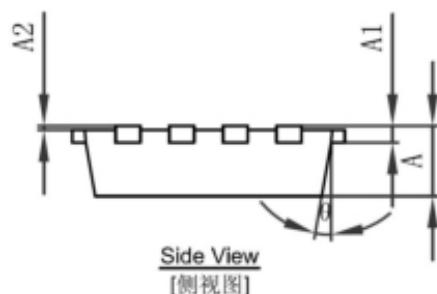
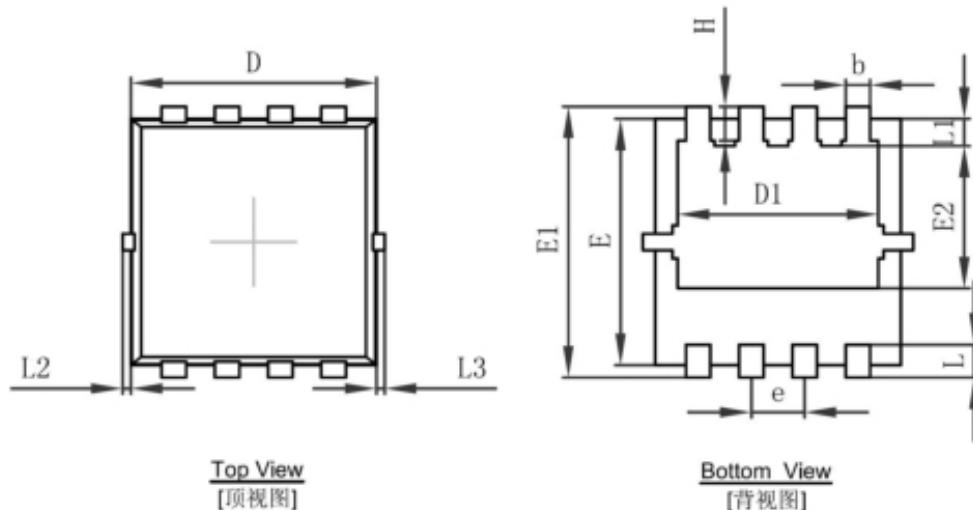


Figure 8. Switching wave

PDFNWB3.3×3.3-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.650	0.850	0.026	0.033
A1	0.152 REF.			0.006 REF.
A2	0~0.05			0~0.002
D	2.900	3.100	0.114	0.122
D1	2.300	2.600	0.091	0.102
E	2.900	3.100	0.114	0.122
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0~0.100			0~0.004
L3	0~0.100			0~0.004
H	0.315	0.515	0.012	0.020
θ	9°	13°	9°	13°