

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
100V	15mΩ@10V	40A
	18mΩ@4.5V	

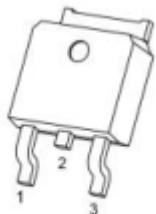
Feature

- Fast Switching
- Low Gate Charge and Rdson
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

Application

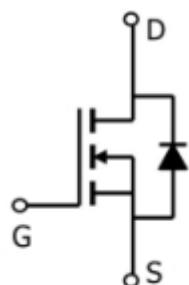
- Power switching application
- DC-DC Converter

Package

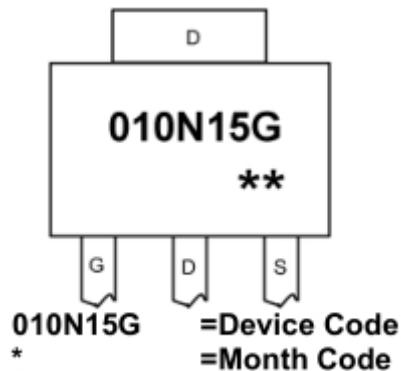


TO-252(1:G 2:D 3:S)

Circuit diagram



Marking



Absolute maximum ratings

($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_c=25^\circ\text{C}$)	I_D	40	A
Pulsed Drain Current ²	I_{DM}	160	A
Single Pulse Avalanche Energy ³	E_{AS}	110	mJ
Total Power Dissipation ⁴ ($T_c=25^\circ\text{C}$)	P_D	72	W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	1.74	$^\circ\text{C}/\text{W}$
Storage Temperature Range	T_{STG}	-55~ +150	$^\circ\text{C}$
Operating Junction Temperature Range	T_J	-55~ +150	$^\circ\text{C}$

Electrical characteristics

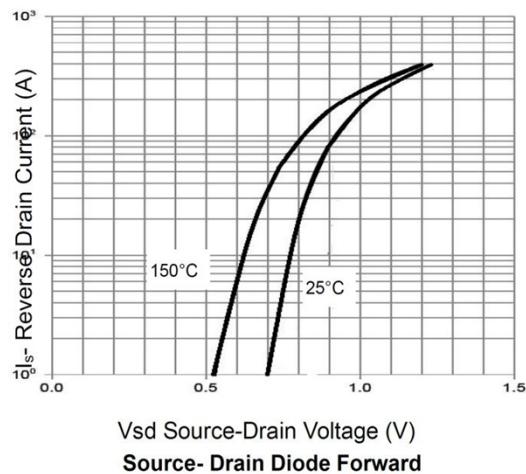
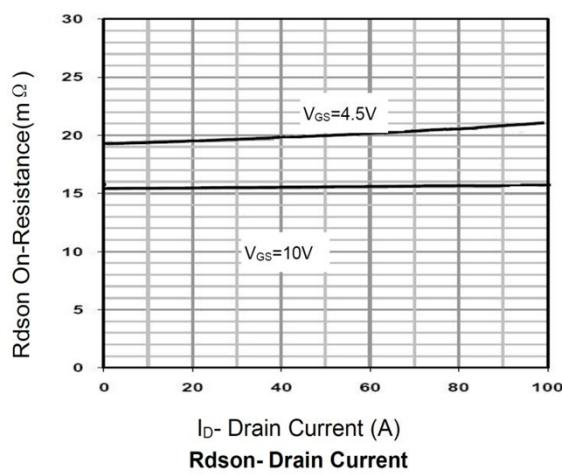
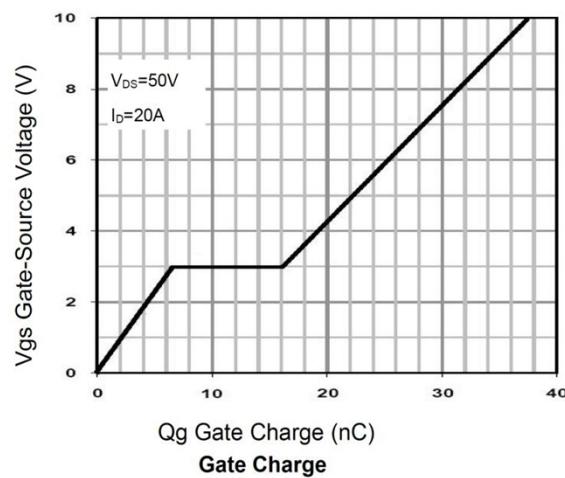
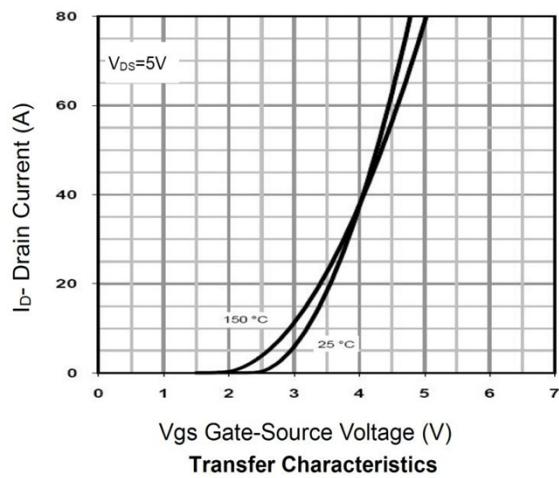
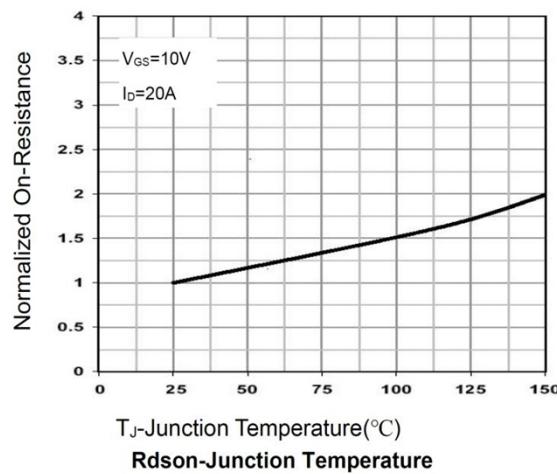
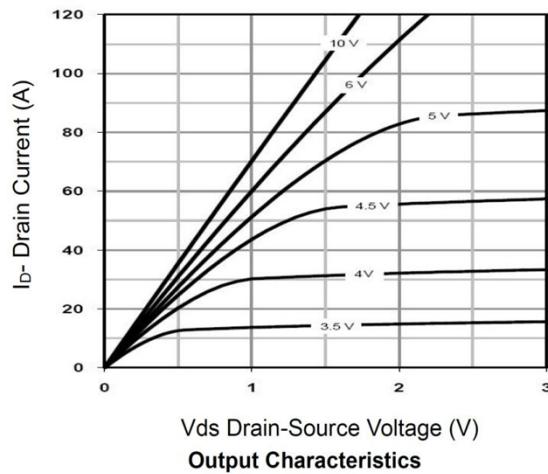
($T_A=25^\circ\text{C}$, unless otherwise noted)

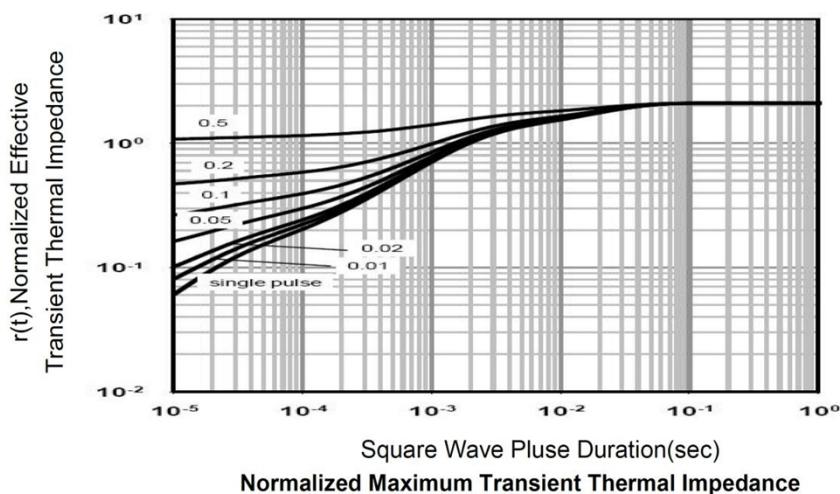
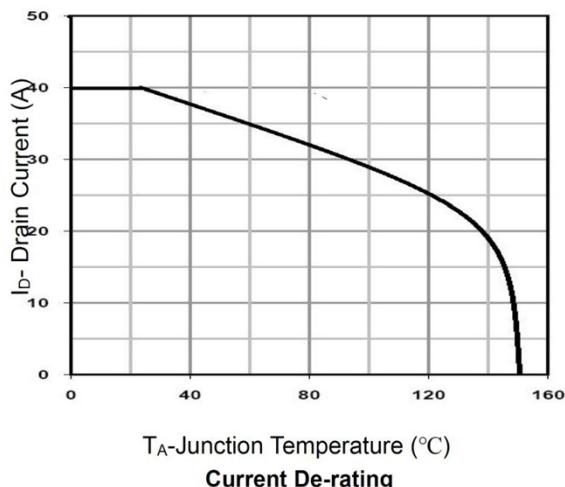
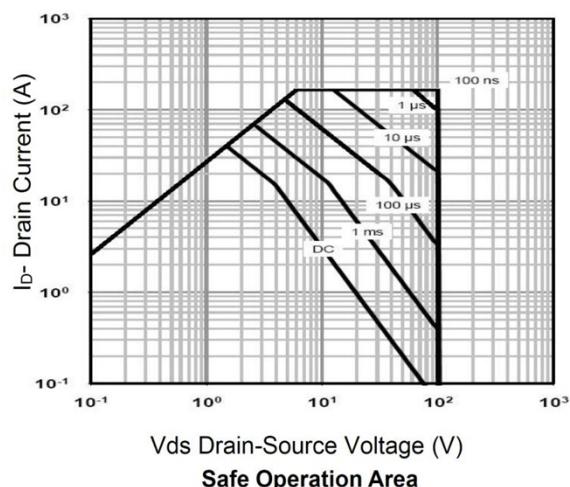
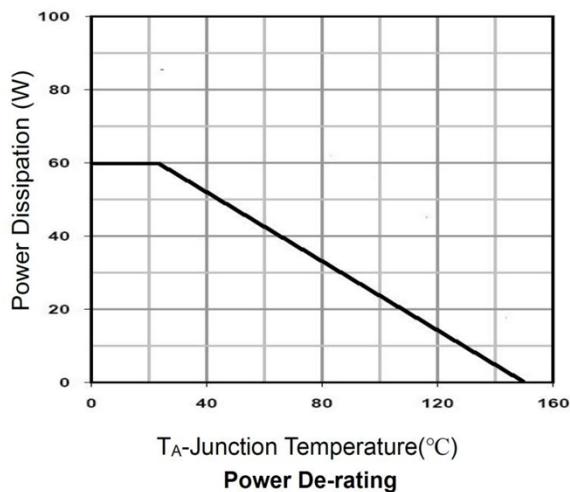
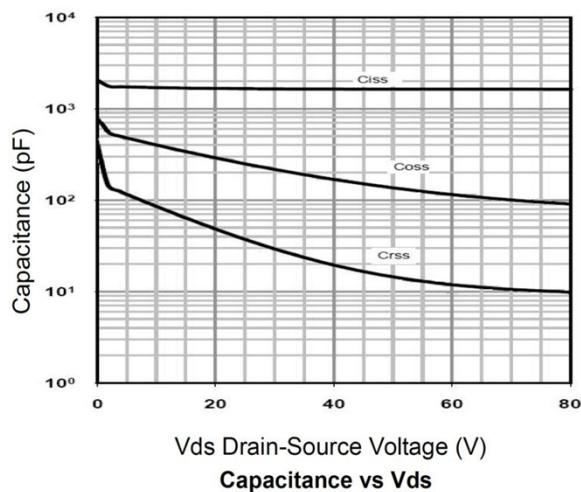
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 80\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$			1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	μA
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.8	2.5	V
Static Drain-Source on-Resistance ²	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		15	19	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 10\text{A}$		18	24	
Dynamic characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 50\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		1871		pF
Output Capacitance	C_{oss}			161		
Reverse Transfer Capacitance	C_{rss}			19		
Switching Characteristics						
Total Gate Charge (4.5V)	Q_g	$V_{\text{DS}} = 50\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		33.5		nC
Gate-Source Charge	Q_{gs}			6.9		
Gate-Drain Charge	Q_{gd}			5.1		
Turn-On Delay Time	$T_{\text{d(on)}}$	$V_{\text{DD}} = 50\text{V}, V_{\text{GS}} = 10\text{V}, R_G = 3\Omega, I_D = 20\text{A}$		15		nS
Rise Time	T_r			18		
Turn-Off Delay Time	$T_{\text{d(off)}}$			30		
Fall Time	T_f			9		
Drain-Source Diode Characteristics						
Diode forward voltage ²	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_s = 1\text{A}, T_J = 25^\circ\text{C}$			1.2	V

Notes:

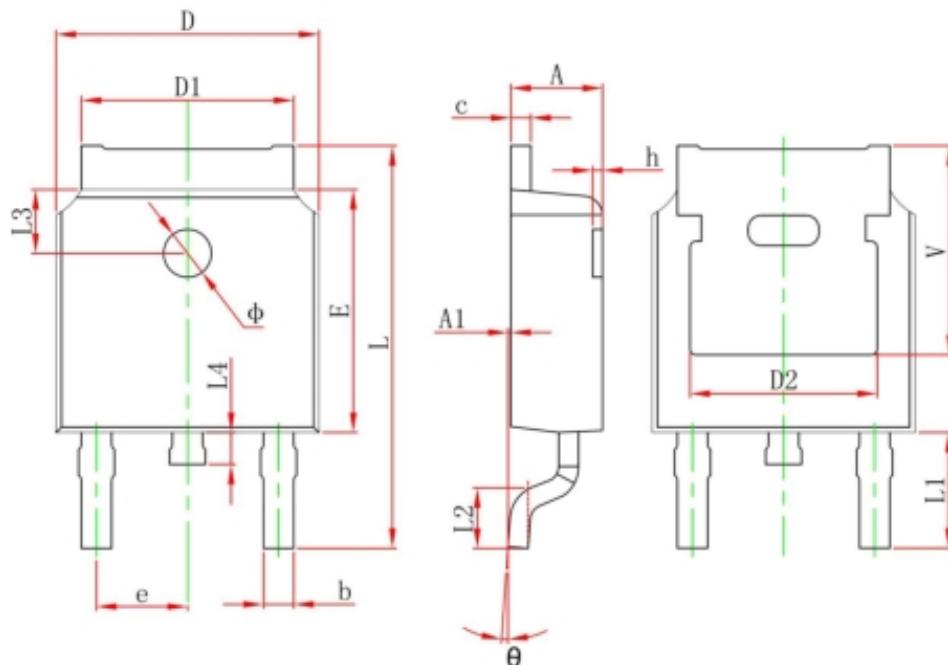
- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating. The test condition is $V_{\text{DD}} = 50\text{V}, V_{\text{GS}} = 10\text{V}, L = 0.5\text{mH}$
- The power dissipation is limited by 150°C junction temperature

Typical Characteristics





TO-252-2L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 REF.		0.211 REF.	