

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
20V	13mΩ@10V	7A
	22mΩ@2.5V	

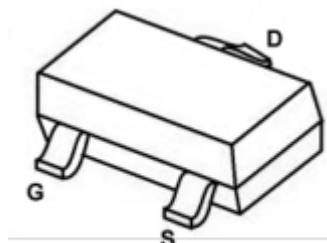
Feature

- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on- resistance and maximum DC current capability
- ESD Protected:2KV

Application

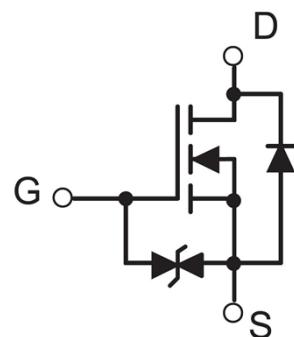
- DC/DC Converter
- Load Switch for Portable Devices
- Battery Switch

Package

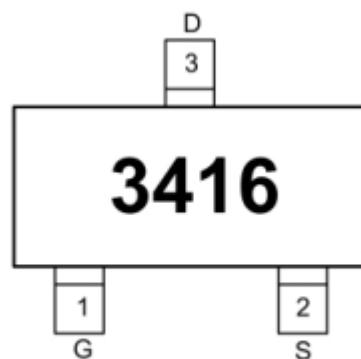


SOT-23

Circuit diagram



Marking



3416 =Device Code

Absolute maximum ratings

($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	7	A
Pulsed Drain Current	I_{DM}	30	A
Power Dissipation	P_D	1.4	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	357	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~+150	$^\circ\text{C}$

Electrical characteristics

($T_A=25^\circ\text{C}$, unless otherwise noted)

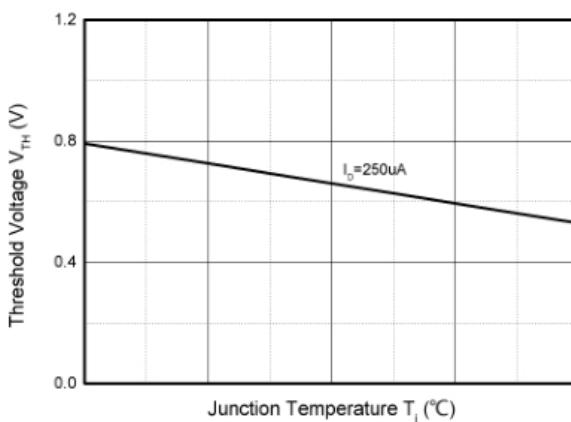
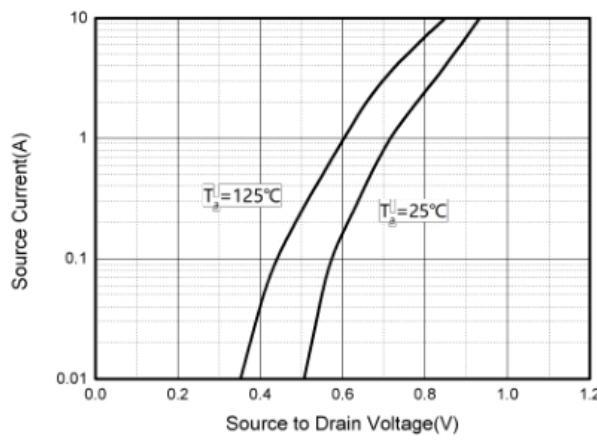
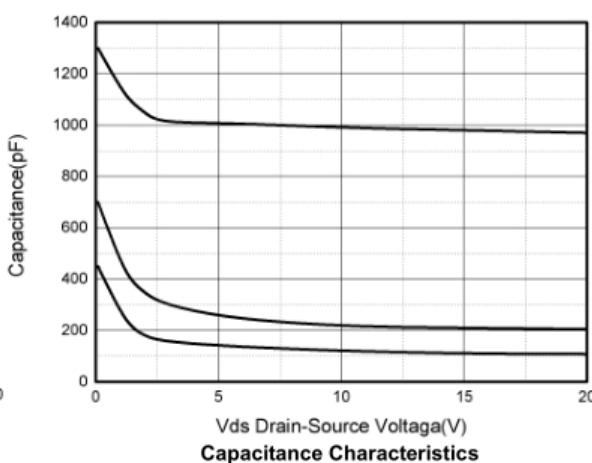
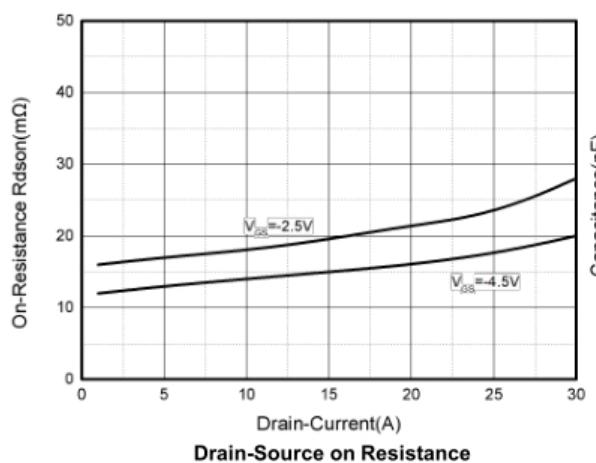
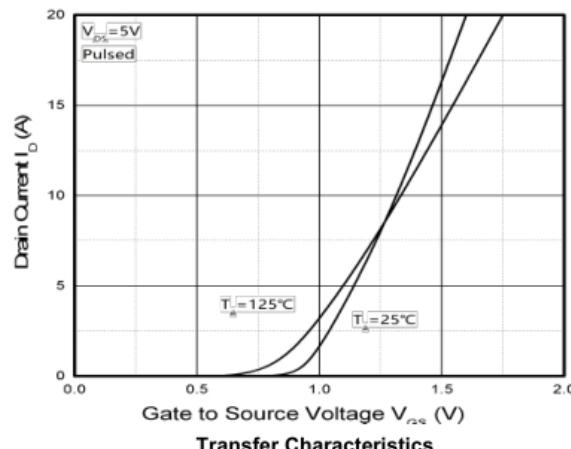
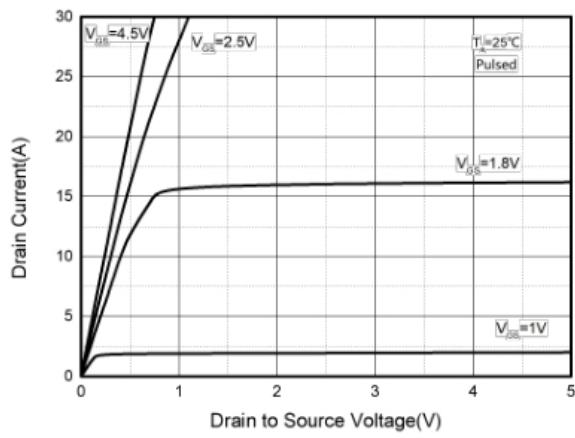
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$\text{BV}_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	20			V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 10\text{V}, V_{DS} = 0\text{V}$			± 10	μA
Gate threshold voltage ⁽¹⁾	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.4	0.65	1.1	V
Drain-source on-resistance	$R_{DS(\text{on})}$	$V_{GS} = 4.5\text{V}, I_D = 7\text{A}$		13	18	$\text{m}\Omega$
		$V_{GS} = 2.5\text{V}, I_D = 4\text{A}$		22	30	
Dynamic Characteristics⁴⁾						
Total gate charge	Q_g	$V_{GS} = 4.5\text{V}, V_{DS} = 10\text{V}, I_D = 7\text{A}$		8.1		nC
Gate-source charge	Q_{gs}			2.4		
Gate-drain charge	Q_{gd}			3		
Input capacitance	C_{iss}	$V_{DS} = 10\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		980		pF
Output capacitance	C_{oss}			225		
Reverse transfer capacitance	C_{rss}			120		
Switching Characteristics⁴⁾						
Turn-on Delay Time	$T_{d(\text{on})}$	$V_{GS} = 4.5\text{V}, V_{DD} = 10\text{V}, R_L = 1.5\Omega, R_{GEN} = 3\Omega$		1.2		nS
Turn-on Rise Time	T_r			2.4		
Turn-Off Delay Time	$T_{d(\text{off})}$			22		
Turn-Off Fall Time	t_f			7		
Source-Drain Diode characteristics						
Diode Forward voltage ⁽³⁾	V_{SD}	$V_{GS} = 0\text{V}, I_S = 7\text{A}$			1.2	V

Note :

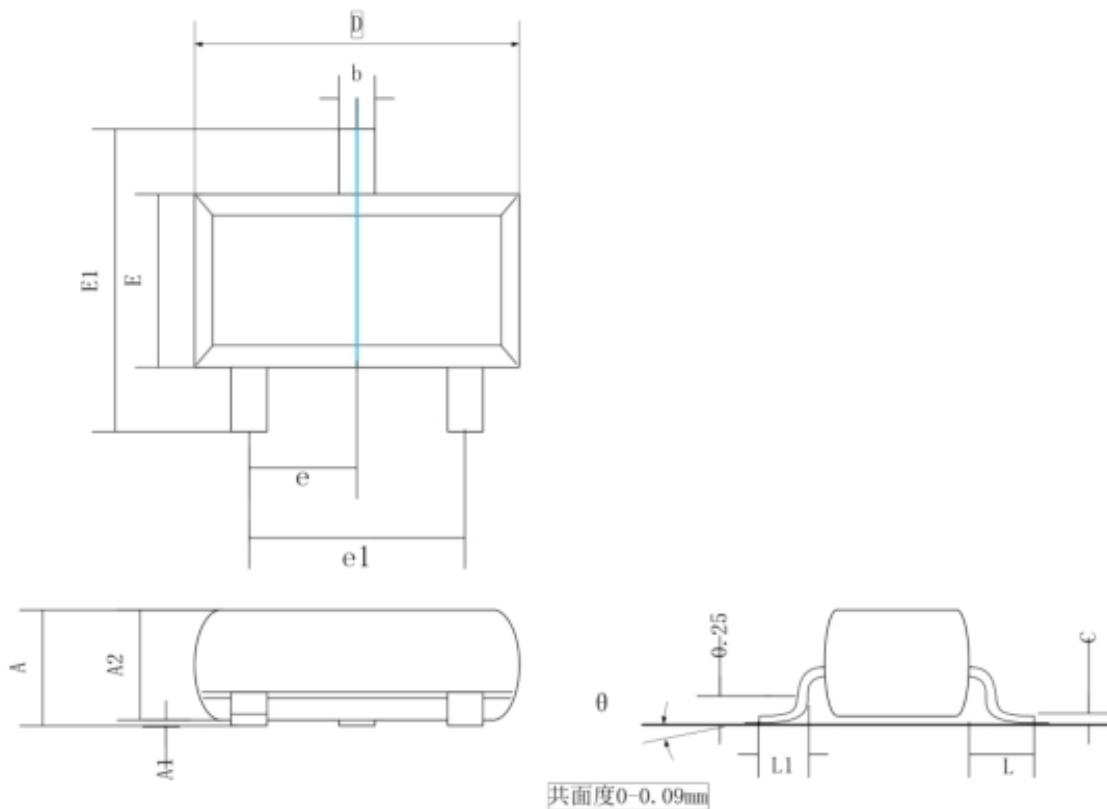
1. Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

2. These parameters have no way to verify.

Typical Characteristics



SOT-23 Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	0.90	1.15
A1	0.00	0.10
A2	0.90	1.05
b	0.30	0.50
c	0.08	0.15
D	2.80	3.00
E	1.20	1.40
E1	2.25	2.55
e	0.95 REF.	
e1	1.80	2.00
L	0.55 REF.	
L1	0.30	0.50
θ	0°	8°