

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
100V	90mΩ@10V	10A
	100mΩ@4.5V	
-100V	-90mΩ@10V	-18A
	-100mΩ@4.5V	

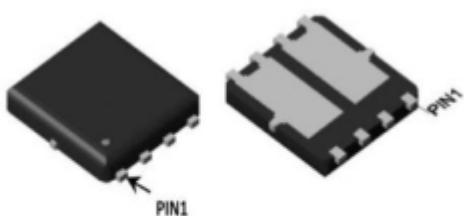
Feature

- TrenchFET Power MOSFET
- Excellent $R_{DS(on)}$ and Low Gate Charge
- Fast Switching Speed

Applications

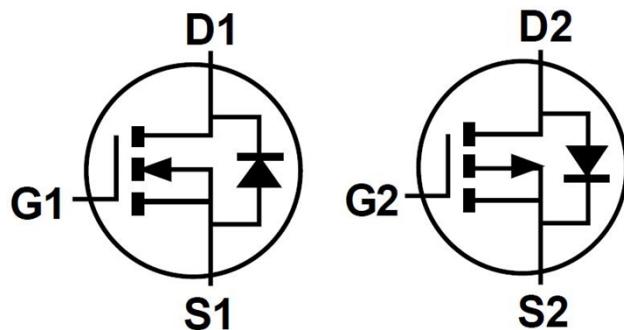
- Motor Control
- Inverters

Package

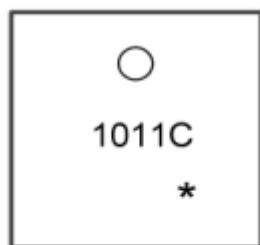


PDFN5×6-8L

Circuit diagram



Marking



1011C = Device code

* = Month Code

Absolute maximum ratings

($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value		Unit
		N-Channel	P-Channel	
Drain-Source Voltage	V_{DS}	100	-100	V
Gate-Source Voltage	V_{GS}	± 20	± 20	V
Continuous Drain Current($t \leq 10\text{s}$)	I_D	10	-18	A
Single Pulse Avalanche Energy	E_{AS}	40	72	mJ
Power Dissipation($t \leq 10\text{s}$)	P_D	30	72	W
Thermal Resistance from Junction to Ambient($t \leq 10\text{s}$)	$R_{\theta JA}$	4.16	1.74	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	150		$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 to 150		$^\circ\text{C}$

N-Channel Electrical characteristics

($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$\text{BV}_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$			± 100	μA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.6	2.5	V
Drain-Source On-State Resistance	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 8\text{A}$		90	115	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 6\text{A}$		100	135	
Dynamic Characteristics						
Input capacitance	C_{iss}	$V_{DS}=50\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$		792		pF
Output capacitance	C_{oss}			34.6		
Reverse transfer capacitance	C_{rss}			35.4		
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS}=50\text{V}, V_{GS}=4.5\text{V}, I_D = 8\text{A}$		14		pF
Gate-Source Charge	Q_{gs}			2.5		
Gate-Drain Charge	Q_{gd}			2.62		
Turn-on Delay Time	$T_{d(\text{on})}$	$V_{DD}=50\text{V}, V_{GS}=10\text{V}, R_G = 1.5\Omega, I_D = 8\text{A}$		4.2		nS
Turn-on Rise Time	T_r			8.2		
Turn-Off Delay Time	$T_{d(\text{off})}$			31		
Turn-Off Fall Time	t_f			4		
Source-Drain Diode Characteristics						
Body Diode Voltage	V_{SD}	$I_s=1\text{A}, V_{GS} = 0\text{V}$			1.2	V

Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. EAS data shows Max. rating . The test condition is $V_{DD}=50\text{V}, V_{GS}=10\text{V}, L=0.5\text{mH}$
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

P-Channel Electrical characteristics

($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$\text{BV}_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$	-100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -80\text{V}, V_{GS} = 0\text{V}$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$			± 100	μA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1	-1.8	-2.5	V
Drain-Source On-State Resistance	$R_{DS(\text{on})}$	$V_{GS} = -10\text{V}, I_D = -8\text{A}$ $V_{GS} = -4.5\text{V}, I_D = -6\text{A}$		90	115	$\text{m}\Omega$
				100	135	
Dynamic Characteristics						
Input capacitance	C_{iss}	$V_{DS} = -50\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		2029		pF
Output capacitance	C_{oss}			129		
Reverse transfer capacitance	C_{rss}			76		
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS} = -50\text{V}, V_{GS} = -4.5\text{V}, I_D = -8\text{A}$		43		pF
Gate-Source Charge	Q_{gs}			9.5		
Gate-Drain Charge	Q_{gd}			5.8		
Turn-on Delay Time	$T_{d(on)}$	$V_{DD} = -50\text{V}, I_D = -1\text{A}, V_{GS} = -10\text{V}, R_{GEN} = 6\Omega$		10		nS
Turn-on Rise Time	T_r			15		
Turn-Off Delay Time	$T_{d(off)}$			110		
Turn-Off Fall Time	t_f			70		
Source-Drain Diode Characteristics						
Body Diode Voltage	V_{SD}	$I_S = -1\text{A}, V_{GS} = 0\text{V}$			1.2	V

Note:

4. Repetitive Rating: Pulse width limited by maximum junction temperature.
6. EAS data shows Max. rating . The test condition is $V_{DD} = -15\text{V}, V_{GS} = -10\text{V}, L = 0.5\text{mH}$
7. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
8. Guaranteed by design, not subject to production

N-Channel Typical Characteristics

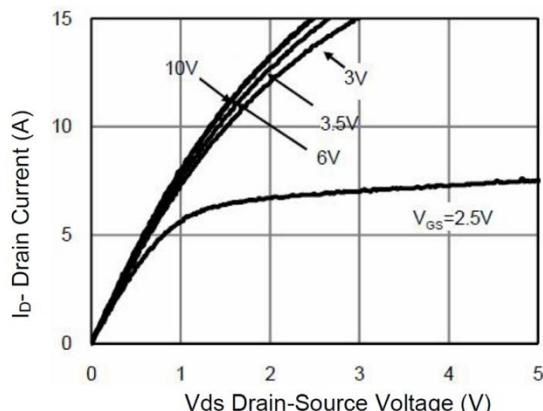


Figure 1 Output Characteristics

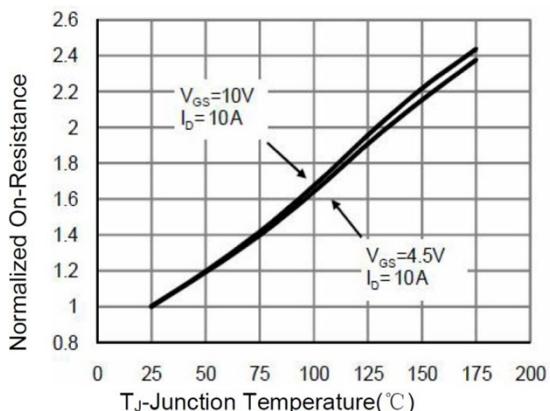


Figure 4 $R_{DS(on)}$ -JunctionTemperature

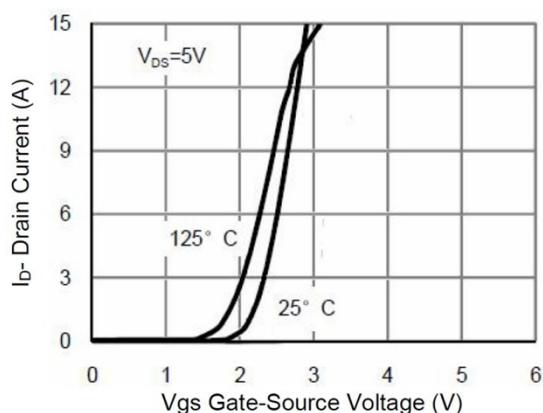


Figure 2 Transfer Characteristics

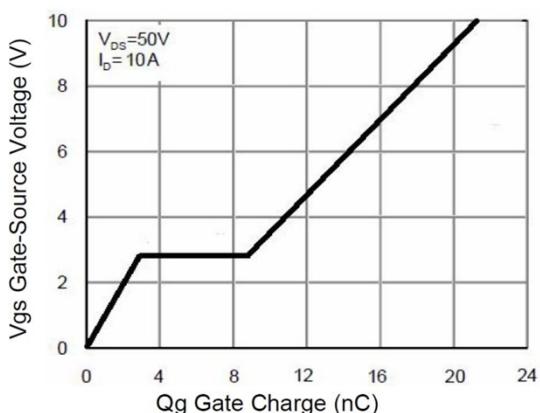


Figure 5 Gate Charge

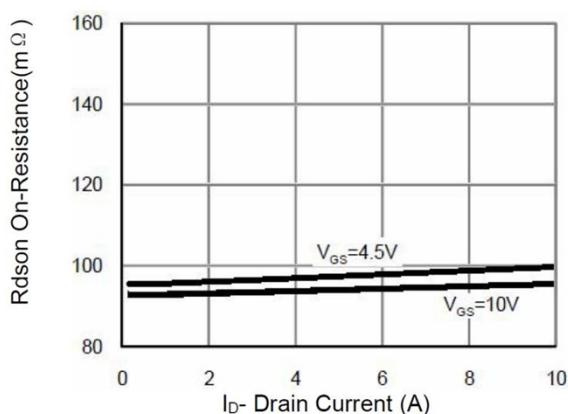


Figure 3 $R_{DS(on)}$ - Drain Current

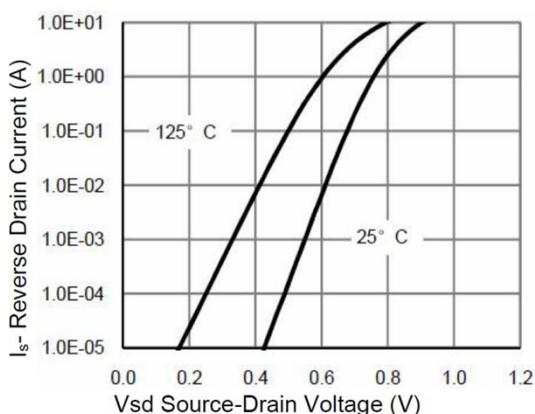
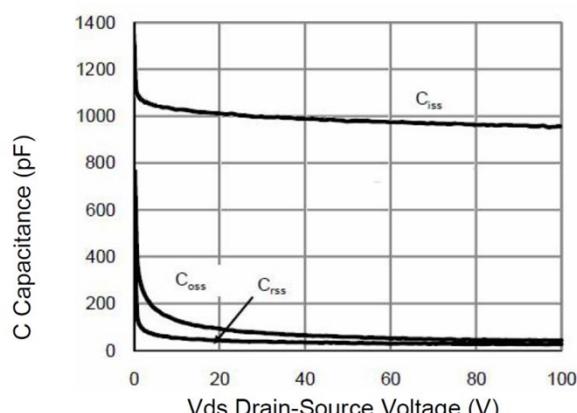
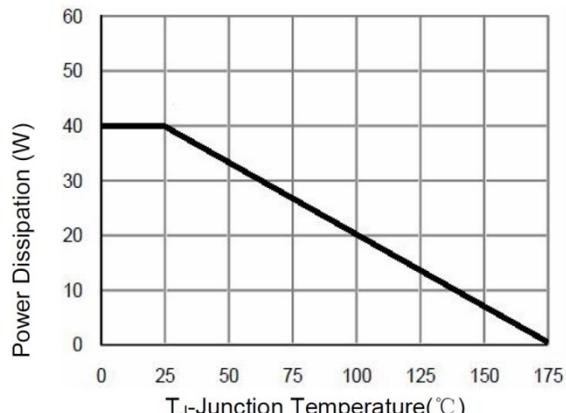
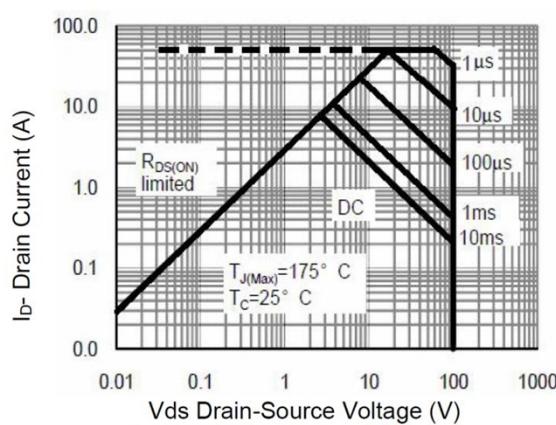
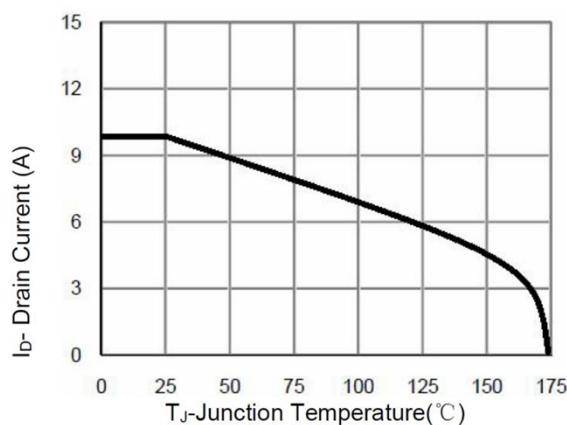
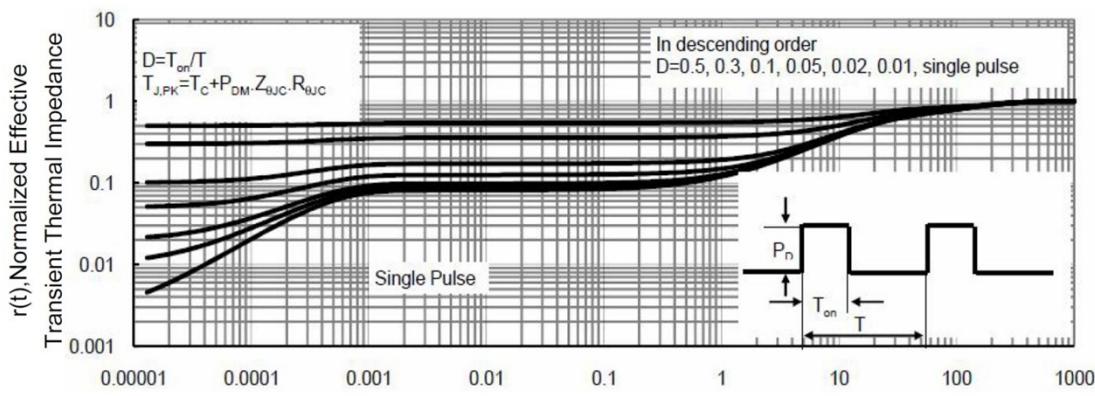
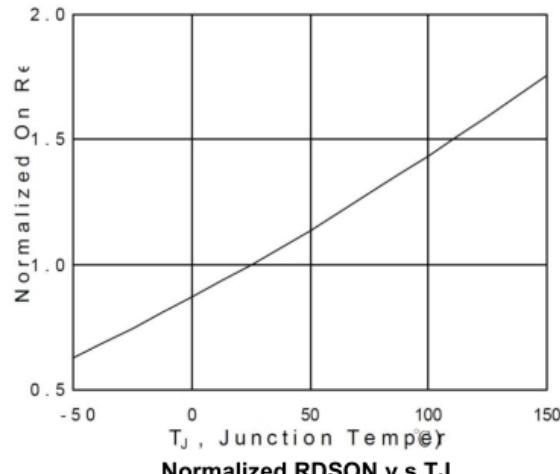
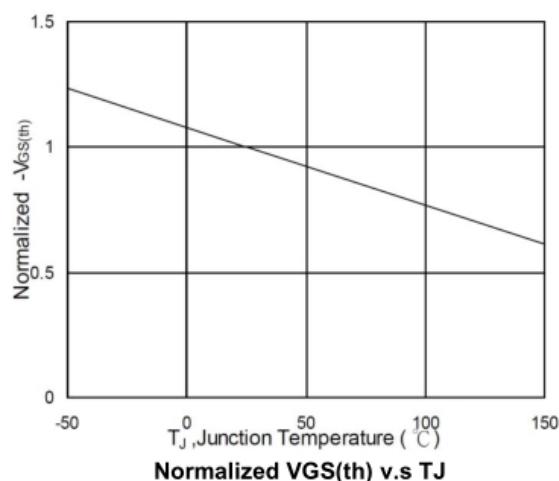
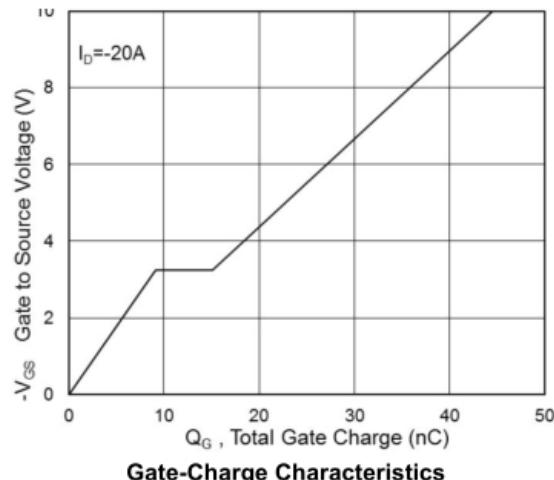
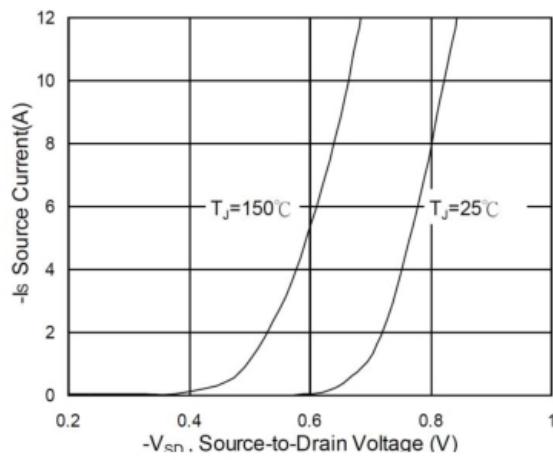
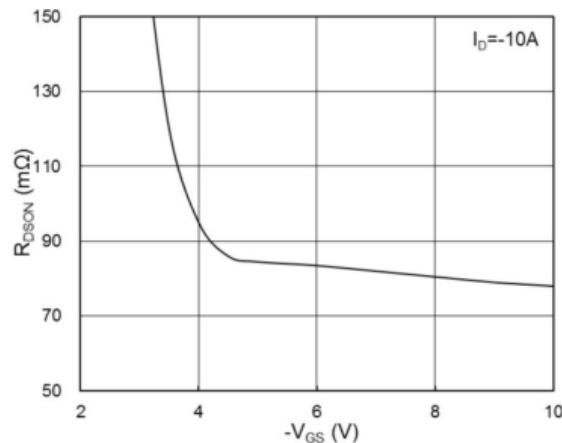
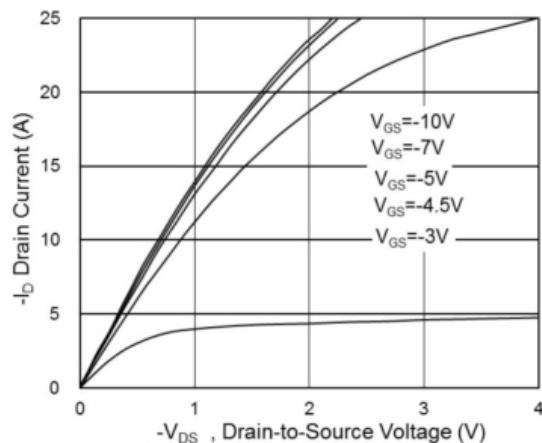
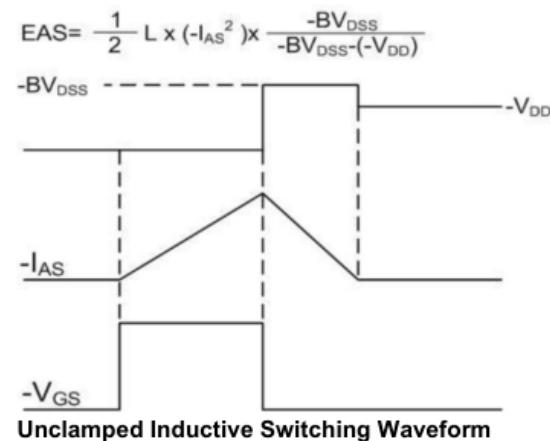
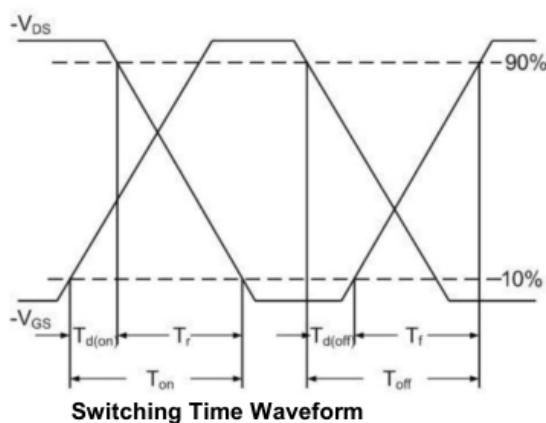
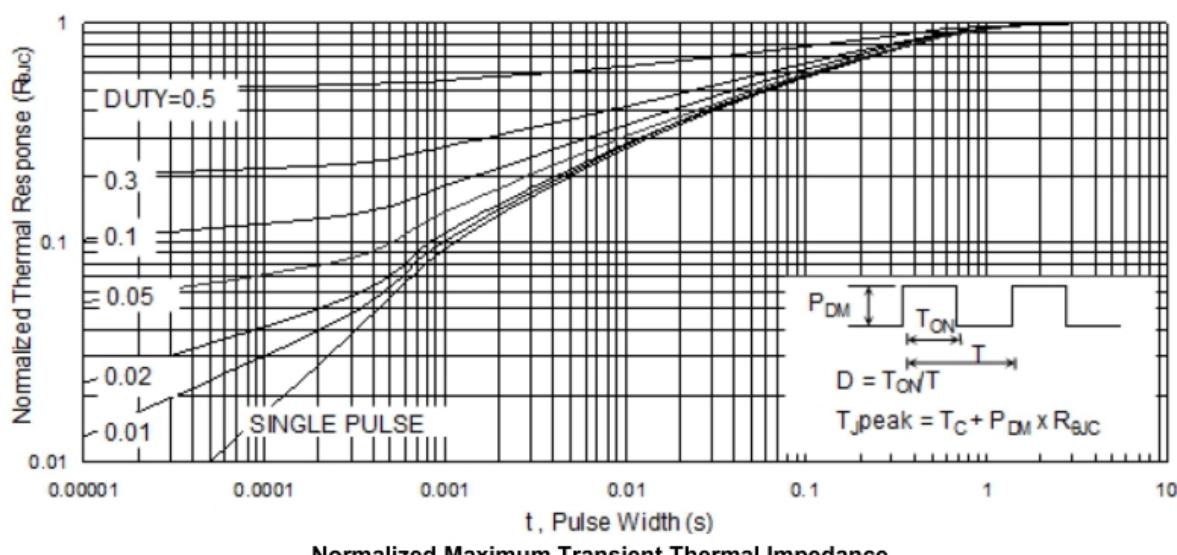
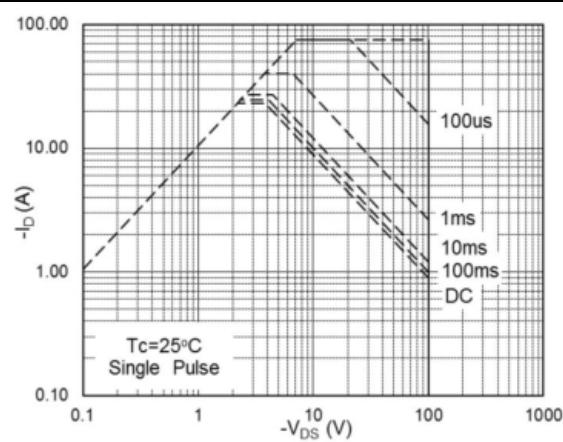
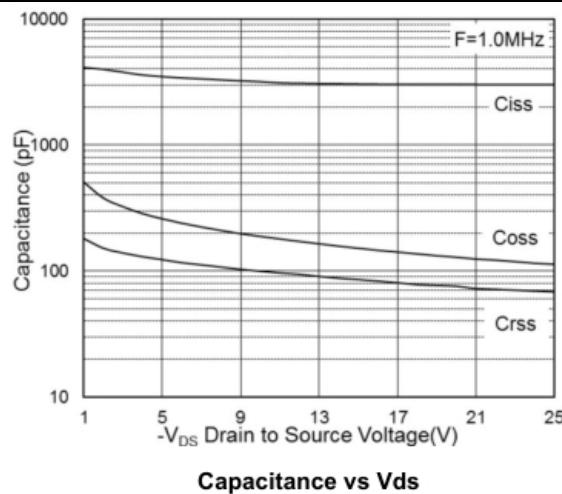


Figure 6 Source- Drain Diode Forward

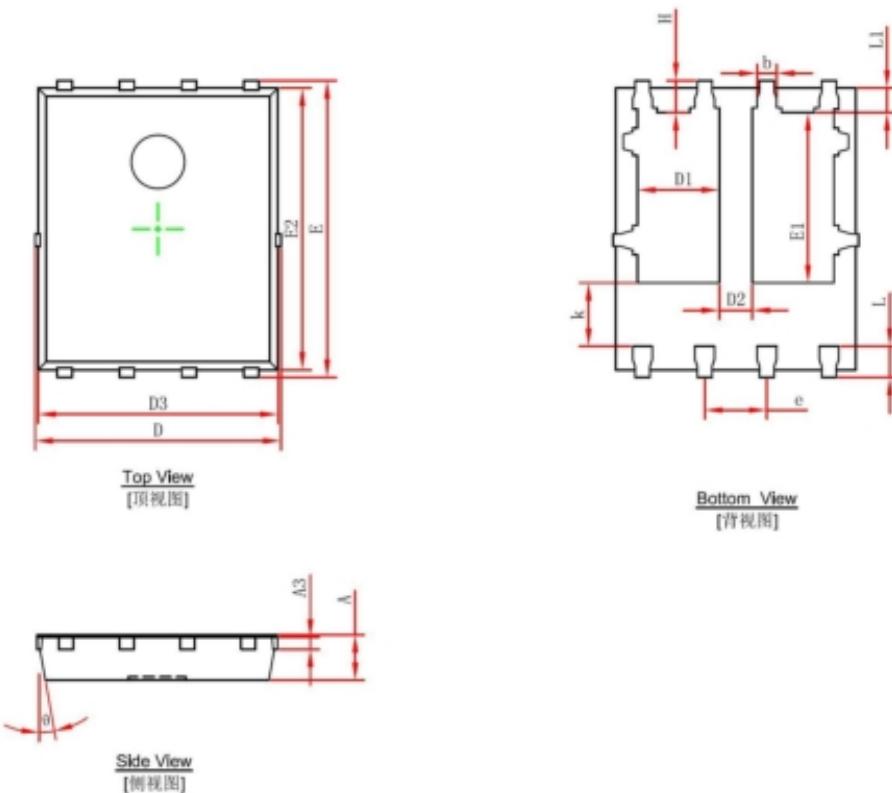

Figure 7 Capacitance vs Vds

Figure 9 Power De-rating

Figure 8 Safe Operation Area

Figure 10 Current De-rating

Figure 11 Normalized Maximum Transient Thermal Impedance

P-Channel Typical Characteristics





PDFN5X6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254 REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	1.470	1.870	0.058	0.074
D2	0.470	0.870	0.019	0.034
E1	3.375	3.575	0.133	0.141
D3	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°